Date: March 9, 2021
Project Title: gearshifft
Usage: Testbed

Principal Investigator:

- Name: Dr.-Ing. Guido Juckeland
- University/Company/Institute: Helmholtz-Zentrum Dresden-Rossendorf (HZDR)
- Mailing address including country:
  Helmholtz-Zentrum Dresden-Rossendorf
  Computational Science (FWCC)
  Herrn Dr.-Ing. Guido Juckeland
  Bautzner Landstr. 400
  01328 Dresden, GERMANY
- Phone number: +49 351 260 3660
- Email: g.juckeland@hzdr.de

Names & Email of initial project users:

- David Pape, d.pape@hzdr.de
- Anthony Curtis, anthony.curtis@stonybrook.edu

Usage Description:

The Fast Fourier Transform is an essential tool in signal analysis and is used across domains, e.g. in natural sciences, engineering, and artificial intelligence. Due to an increasing amount of experimental and simulation data, performant FFT calculations are a crucial aspect of modern science. Recent changes in the TOP500 list indicate a shift from x86-dominated HPC centers toward an increased availability of ARM and POWER processors. This motivates an analysis of the performance of well-established FFT algorithms and different implementations on these systems.
gearshifft is an FFT benchmark suite that was developed to allow for comparison between different CPU, GPU, as well as heterogeneous systems. As part of a master’s thesis, support for ARM Performance Libraries was added to gearshifft and the performance of an x86, aPOWER and a last generation ARM processor was assessed. In the process, special attention was payed to specific use cases of the scientific staff at HZDR. Furthermore, Roofline analysis was performed to get a better understanding of the effect of the memory hierarchy on FFT performance.

Since SIMD usage of the vendor libraries has been shown to have a great influence on FFT performance, ARM’s Scalable Vector Extensions are an addition to the ARM platform that makes evaluation of FFT performance on Ookami worthwhile.

Together with researchers from Anthony Curtis’ group, we plan to build a roofline model for the A64FX processor using performance counter data and then run a number of FFT use case scenarios to augment the already published performance data.

Computational Resources:

- Total node hours per year: 500
- Size (nodes) and duration (hours) for a typical batch job: single node, up to 1h for FFT benchmarks, around 24 hours for empirical roofline analysis
- Disk space (home, project, scratch): 1GB, 5GB, 5GB

Personnel Resources:

An exchange of experiences on the usage of floating point performance counters would be appreciated.

Required software:

- CMake
- GCC C++ Compiler
- ARM Performance Libraries
- FFTW3 with OpenMP support
- Boost.System, Boost.Test, Boost.ProgramOptions
- Score-P
- Python 3