The goal of this course is to provide in-depth understanding of modern parallel computer architectures and the introduction to parallel programming with ISPC, Pthreads, OpenMP, MPI, and CUDA. We will seek to understand the fundamental design issues, engineering tradeoffs, and essential interplay of hardware and software that cut across parallel machines, rather than simply consider a descriptive taxonomy. The emphasis is on shared memory and data parallel systems. Students will undertake a system design/parallel programming project either of their own choice (that has to be approved by Instructor) or the "default" one on multi-core VHDL design suggested by Instructor.

Prerequisites: ESE 545 or equivalent. 3 credits


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Textbooks (recommended):


Course Grading:

- **Individual** parallel programming assignments with CUDA, POSIX threads, OpenMP, and ISPC - 40%
- Mid-term exam - 25%
- Project (one- or two-student teams) - 35%
If you have a physical, psychological, medical or learning disability that may impact on your ability to carry out assigned course work, I would urge that you contact the staff in the Disabled Student Services office (DSS), room 133 Humanities, 632-6748/TDD. DSS will review your concerns and determine, with you, what accommodations are necessary and appropriate. All information and documentation of disability is confidential.