ESE 516
Integrated Electronic Devices and Circuits I

Fall 2020

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Classes: W 4:25pm-7:15pm, on-line
Classes will be delivered synchronously using Zoom at the scheduled class time. The lecture notes will be posted before and after the class (include handwritten notes during the class) on Blackboard.
Office hours: MW 10am-12pm, or by appointment using Zoom

Recommended Textbook:

References:

Course Description
This is an advance circuit design course that will discuss the principles, concepts and techniques required to produce successful designs of analog integrated circuits. Fundamentals of devices, circuits and basic topologies will be reviewed. Topics of noise, distortion, mismatch, feedback and frequency compensation will be covered in the class. The emphasis will be on the design of high-performance operational amplifiers.

Course Schedule

Week 1  Introduction to VLSI technology, fundamentals of diode and MOS transistor. MOS transistor modeling.
Week 2  Single-stage amplifiers.
Week 3  "Small-signal/moderate frequency" transistor models. Frequency response of single-stage amplifiers.
Week 4  Basic analog building blocks: current mirrors and biasing.
Week 5  Differential pair.
Week 6  Noise, distortion and mismatch.
Week 7  High-gain amplifiers: "classic" two-stage amplifier.
Week 8  High-gain amplifiers: "current-mirror" amplifier, "telescopic" and "folded cascode" architectures.
Week 10 Deviations from idealities of practical op-amps and designs that minimize some of them.
Week 11 Common-mode feedback.
Week 12 Nanometer design. gm/Id design methodology.
Week 13 Biasing of the amplifiers. Bandgap references.

Goals:
The purpose of this course is to introduce students to principles of analysis and design of analog integrated circuits, starting from single transistor circuits to the multi-stage operational amplifier design.

Objectives:
Students should be able to:
1) analyze and design single-stage amplifier
2) analyze and design multi-stage differential amplifiers
3) analyze the frequency response of a single-stage and multi-stage amplifier
4) design a high-gain amplifier based on defined set of performance parameters.

Homework:
To help prepare for the midterm and final exams, homework exercises will be assigned. They will not be graded. Solutions will be given a week after they are assigned. The example problems and exams will be provided prior to both exams.

Project:
The project will include a set of three Cadence assignments: single-stage amplifier simulation and design and two-stage operational amplifier simulation. The final part of the project is the design problem: design of a high-gain amplifier based on defined set of performance parameters.

Students can do project by themselves or they can form a group comprising 2 members (single project report required in the case of 2 member group).

The students will be able to access department linux machines and run Cadence remotely.

Credit Distribution
1. Midterm (30%): will be held in on-line form on October 14th
2. Final (40%): will be held in on-line form in the finals week
3. Project (30%)
Access to our class's on-line Blackboard site:

You can access class information on-line at: http://blackboard.stonybrook.edu
If you have used Stony Brook's Blackboard system previously, your login information (Username and Password) has not changed. If you have never used Stony Brook's Blackboard system, your initial password is your SOLAR ID# and your username is the same as your Stony Brook (sparky) username, which is generally your first initial and the first 7 letters of your last name.

For help or more information see:
http://www.sinc.sunysb.edu/helpdesk/docs/blackboard/bbstudent.php

For problems logging in, go to the helpdesk in the Main Library SINC Site or the Union SINC Site, you can also call: 631-632-9602 or e-mail: helpme@ic.sunysb.edu

Electronic Communication Statement:

Email and especially email sent via Blackboard (http://blackboard.stonybrook.edu) is one of the ways the faculty officially communicates with you for this course. It is your responsibility to make sure that you read your email in your official University email account. For most students that is Google Apps for Education (http://www.stonybrook.edu/mycloud), but you may verify your official Electronic Post Office (EPO) address at http://it.stonybrook.edu/help/kb/checking-or-changing-your-mail-forwarding-address-in-the-epo.

If you choose to forward your official University email to another off-campus account, faculty are not responsible for any undeliverable messages to your alternative personal accounts. You can set up Google Mail forwarding using these DoIT-provided instructions found at http://it.stonybrook.edu/help/kb/setting-up-mail-forwarding-in-google-mail.

If you need technical assistance, please contact Client Support at (631) 632-9800 or supportteam@stonybrook.edu.

Americans with Disabilities Act:

If you have a physical, psychological, medical or learning disability that may impact your course work, please contact Disability Support Services, ECC (Educational Communications Center) Building, room128, (631) 632-6748. They will determine with you what accommodations, if any, are necessary and appropriate. All information and documentation is confidential.

Academic Integrity:

Each student must pursue his or her academic goals honestly and be personally accountable for all submitted work. Representing another person's work as your own is always wrong. Faculty are required to report any suspected instances of academic dishonesty to the
Academic Judiciary. Faculty in the Health Sciences Center (School of Health Technology & Management, Nursing, Social Welfare, Dental Medicine) and School of Medicine are required to follow their school-specific procedures. For more comprehensive information on academic integrity, including categories of academic dishonesty, please refer to the academic judiciary website at http://www.stonybrook.edu/uaa/academicjudiciary/

**Critical Incident Management:**

Stony Brook University expects students to respect the rights, privileges, and property of other people. Faculty are required to report to the Office of Judicial Affairs any disruptive behavior that interrupts their ability to teach, compromises the safety of the learning environment, or inhibits students' ability to learn. Faculty in the HSC Schools and the School of Medicine are required to follow their school-specific procedures.