



Stony Brook  
University

## *Graduate Student Guide*

Department of Electrical & Computer Engineering

***Fall 2017***

**Note: All Electrical & Computer Engineering graduate students will be expected to read this guide before consulting academic advisors.**

# **CONTENTS**

<b>Introduction</b>	Page 3
<b>Graduate Programs</b>	Page 4-5
<b>Certificate Programs</b>	Page 6-7
<b><u>Electrical Engineering Major</u></b>	Page 8-15
• <b>Degree Requirements:</b>	
➤ M.S. in EE non-thesis option	Page 9
➤ M.S in EE thesis option	Page 10
➤ Ph.D. in EE	Page 11-12
• <b>Policies</b>	Page 13-15
<b><u>Computer Engineering Major</u></b>	Page 16-24
• <b>Degree Requirements:</b>	
➤ M.S. in CE non-thesis option	Page 17-18
➤ M.S in CE thesis option	Page 18-19
➤ Ph.D. in CE	Page 20-21
• <b>Policies</b>	Page 22-24
<b>Area courses</b>	Page 25
<b>Teaching/Graduate Assistant/Tuition Information</b>	Page 26-27
<b>Curricular Practical Training</b>	Page 28
<b>Facilities</b>	Page 29-30
<b>Faculty</b>	Page 31-32
<b>Course Listing</b>	Page 33-45
• Outside of Dept. Course List for CE Majors	Page 46

## INTRODUCTION

This *Guide* has been written to provide general information to graduate students in the Department of Electrical and Computer Engineering. It is intended to give some assistance and guidance to students regarding routine and other procedures involved in the pursuit of their program. This *Guide* is intended to be an informal one. It supplements, but does not replace, the official University *Graduate Bulletin* as a source of information.

The choice of a program to a large degree is the responsibility of the student. The members of the graduate committee are available for consultation on any point in this *Guide* that may be unclear to the student. Students may undertake an independent project under the direction of a faculty member, who often becomes the student's best source of advice on professional, academic, and financial support matters. Students should select their research advisor within the first year of the program but only after a careful canvass among the professors in their field of interests.

---

# **GRADUATE PROGRAMS**

## **M.S. AND PH.D. DEGREES**

The Department offers graduate programs leading to the M.S. and Ph.D. Degrees in Electrical or Computer Engineering. Graduate Programs are tailored to the needs of each student so as to provide a strong analytical background helpful to the study of advanced engineering problems. Ample opportunities exist for students to initiate independent study and to become involved in active research programs, both experimental and theoretical.

## **CHANGE OF MAJOR POLICY**

A change of major from Electrical Engineering to Computer Engineering or vice versa will only be permitted once in a student's graduate career, (provided the student meets degree requirements and retains Department approval). Students cannot return to their original major once they have completed the change.

## **AREAS OF EMPHASIS**

Areas of emphasis in current research and instruction are:

- (1) Communications & Signal Processing
- (2) Computer Engineering
- (3) Semiconductor Devices and Quantum Electronics
- (4) Circuits and VLSI

Specialties that fall under one or more of the above categories include: VLSI, Image Processing, Computer Vision, Integrated Circuit Fabrication, Novel Electronic and Photonic Devices, Digital Communication, Biomedical Electronics, Computer-Aided Design, Computer Networks, Parallel Processing, Fault-Tolerant Computing, Microprocessors, Robotics, Network Theory, and Optical Signal Processing and Fiber Optic Sensors. Theoretical and experimental programs reflecting these areas are currently underway and students are encouraged to actively participate in these efforts. Outlined below is an overview of the Department's research areas.

## **COMMUNICATIONS AND SIGNAL PROCESSING**

Subject areas of current interest include mobile, wireless and personal communications; high speed data and computer communication networks; communications traffic; data compression; coding and modulation techniques; inter-connection networks and high speed packet switching; digital communication; detection and estimation; statistical signal processing; spectrum estimation; image analysis and processing; computer vision.

## **COMPUTER ENGINEERING**

The goal of computer engineering in the ECE Department is to provide a balanced view of hardware and software issues. The expertise in the program include parallel and/or high performance computer architecture, embedded microprocessor system design, fault tolerant computing, design communications and signal processing, parallel and distributed computing, computer networks, computer vision, artificial neural networks and software engineering.

## **SEMICONDUCTORS DEVICES, QUANTUM ELECTRONICS**

The program of courses and of research pertinent to solid-state electronics, electromagnetics and optics ranges from a study of the fundamental electronic processes in solids through a description of the mechanism which yield useful devices to a study of the design simulation, and fabrication of integrated circuits. The program's scientific interests center on physics, characterization and development of optoelectronic devices and systems. Over the past several years, major efforts were focused on the studies of physics of semiconductor lasers and detectors. Additionally, the Department has a strong experimental effort on the development of coherent optical processors, fiber optic sensors and integrated fiber optics.

## **CIRCUITS AND VLSI**

The program in the Circuits and VLSI area addresses problems associated to modeling, simulation, design and fabrication of analog, digital, and mixed-signal integrated circuits. Analog and mixed-mode integrated circuit (IC) devices have important applications in many fields including avionics, space technology, and medical technology. The Department offers basic and advanced courses covering the following subjects: integrated circuit technology, device modeling, software tools for circuit design and simulation, analog circuit design, VLSI circuits, testing of analog and digital ICs, design automation for analog, digital and mixed-mode circuits, VLSI systems for communications and signal processing.

## **RELATIONS WITH INDUSTRY**

The Department has joint efforts and close contact with many industrial firms. Areas include: VLSI, CAD, Controls, Digital Signal Processing, Telecommunications, Fiber Optic Sensors and Engineering Education.

# **Certificate Program in Networking and Wireless Communications**

Networking and wireless communications are key technologies in today's technological world. Networks such as the Internet as well as telephone, cable and wireless networks serve to interconnect people and computers in a ubiquitous and cost effective way. The area of wireless communications in particular has grown rapidly in recent years and has utilized networking technology to be successful. There is a large industrial base involving networking and wireless communications in terms of equipment and software providers, service providers and end users. Moreover this technology has made the average consumer's life more productive, flexible and enjoyable.

The Stony Brook Certificate Program in Networking and Wireless Communications is designed to give matriculated students validated graduate level instruction in this area of much recent interest. The program can be completed in a reasonable amount of time as it involves four courses. These are regular Stony Brook graduate level courses taught by Stony Brook faculty. The SUNY approved certificate program can be tailored to the needs of the individual student. Courses used for the certificate program can also be used toward the MS or PhD degree by matriculated students.

## **Admission to the certificate program is limited to students enrolled in either the MS or PhD programs in the Dept. of Electrical & Computer Engineering.**

(Students may receive the certificate if they have no more than 12 graduate credits in the department as of the start of Fall 2017.)

To apply for the Networking and Wireless Communications Certificate Program, a student must complete the "Permission to Enroll in a Secondary Certificate Program" form from the Graduate School website, and submit it to the ECE Department Graduate Program Coordinator prior to the start of the semester. The student must then complete **FOUR** required courses as specified below, with at least a B grade in each course.

### **At least ONE course from the following:**

- ESE 505: Wireless Communications
- ESE 506: Wireless Networking

### **At least ONE course from the following:**

- ESE 532: Theory of Digital Communications
- ESE 546: Networking Algorithms and Analysis
- ESE 548: Local and Wide Area Networks

### **In addition to the above, if needed, courses may be selected from:**

- ESE 503: Stochastic Systems
- ESE 504: Performance Evaluation of Communication and Computer Systems
- ESE 522: Fiber Optic Systems
- ESE 528: Communication Systems
- ESE 531: Detection and Estimation Theory
- ESE 535: Information Theory and Reliable Communication

- ESE 536: Switching and Routing in Parallel and Distributed Systems
- ESE 543: Mobile Cloud Computing
- ESE 544: Network Security Engineering
- ESE 547: Digital Signal Processing
- ESE 550: Network Management and Planning
- ESE 552: Interconnection Networks

*Students are permitted to use up to 6 graduate transfer credits toward the certificate with the approval of the Director. These credits must be from an institution authorized to grant graduate degrees by recognized accredited commission. Credits must not have been used to fulfill the requirements for another degree/certificate; credits must not be more than 5 years old at the time the student is admitted to graduate study at SBU; a course listed as both graduate and/or undergraduate level will not be considered for transfer; credits must carry a letter grade of B or higher.*

After completing the necessary courses, students must request/apply for completion of the certificate program on SOLAR or through the Graduate School website.

**Important Note: Students will not be permitted to extend their time in the MS or PhD degree program based on entering the certificate program. The student MUST complete the secondary certificate within the time limit for the original program.**

Any questions about the Certificate Program should be directed to Professor Thomas Robertazzi via email [Thomas.Robertazzi@stonybrook.edu](mailto:Thomas.Robertazzi@stonybrook.edu)

---

The ECE department is in the process of seeking approval for two new certificates:

- Engineering Machine Learning Systems: grade B or better for ESE 503, ESE 588, ESE 589, and one elective (ESE 568 or ESE 587 or BMI 511 / ESE 569).
- Engineering the Internet of Things: grade B or better for ESE 566, ESE 506, ESE 525, and one elective (ESE 534 or ESE 544).

These programs are not yet approved (applied for approval in summer 2017) and there is no guarantee that they will be approved. Students will be informed when and if the new certificate programs are approved.

On the assumption that “Engineering Machine Learning Systems” will be approved students should take the Fall 2017 ESE 503 Stochastic Systems class and may possibly take as a certificate elective BMI 511/ESE 569 Translational Bioinformatics.

Course offering for “Engineering the Internet of Things” will begin at the earliest in Spring 2018.

---

# ***ELECTRICAL ENGINEERING***

# ELECTRICAL ENGINEERING DEGREE REQUIREMENTS

## I. M.S. in *Electrical Engineering* NON-THESIS OPTION

**Please Note: Full-time M.S. students typically finish the Master's Degree in three (3) semesters.**

1. At least 30 graduate credits with a cumulative and departmental grade point average of 3.0 or better. Among these 30 credits, up to six credits may be ESE 597, ESE 599, or ESE 698.

Only 3 credits of ESE 698 may be used. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.

2. Minimum of eight (8) regular courses. Of these eight, at least seven (7) regular courses must be taken in the Department; three of the seven must be selected from the following CORE Courses:

ESE 502: Linear Systems;  
ESE 503: Stochastic Systems;  
ESE 511: Solid-State Electronics;  
ESE 520: Applied Electromagnetics;  
ESE 528: Communication Systems or ESE 532: Theory of Digital Communication;  
ESE 545: Computer Architecture;  
ESE 554: Computational Models for Computer Engineers;  
ESE 555: Advanced VLSI Circuit Design.

3. ESE 597, ESE 599, ESE 697, ESE 698 and ESE 699 are not counted as regular courses in (2). Topics course, ESE 670, can be counted only once as a regular course.

4. At least one (maximum three) credit of ESE 597. In exceptional circumstances, the Graduate Program Director can approve a replacement of ESE 597 credit by ESE 599, ESE 699 or ESE 698.

Credits for ESE 597 can only be applied toward the electrical engineering degree if the following requirements are satisfied:

Prior approval from the Graduate Program Director based on the student submitting a proposal and securing an advisor in the ECE Department as well as a contact person at the company involved. Approval will only be granted if it can be demonstrated that the faculty advisor will be kept in close touch with work on the project. To this end, practicum not in the local geographic area will be discouraged.

To obtain satisfactory credit the faculty advisor must verify that a substantial engineering project was undertaken and completed. This will be based on his close contact during the entire period of the project with the student and the contact person and upon reviewing a mandatory written report submitted by the student at the project's completion. The faculty advisor will determine the final grade for the course.

CPT will be approved one time and can be taken during the summer or the academic year.

**Extensions of CPT will not be granted.**

*A candidate for the master's degree may petition to transfer a maximum of 12 graduate credits from another institution towards the master's degree requirements. Students transferring from non-matriculated status are also limited to a maximum of 12 credits for the master's degree.*

## II. M.S. in *Electrical Engineering* THESIS OPTION.

**Please Note: Full-time M.S. students typically finish the Master's Degree in four (4) semesters.**

1. Students must inform the Department in writing at the end of their first semester if they would like to choose the M.S. Thesis Option.

2. At least 30 graduate credits with a cumulative and departmental grade point average of 3.0 or better. Among these 30 credits, at least six credits of ESE 599, with a maximum of 12 credits in total being taken from ESE 597, ESE 599, or ESE 698.

Only 3 credits of ESE 698 may be used. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.

3. Minimum of six (6) regular courses. Of these six, at least five (5) regular courses must be taken in the Department; three of the five must be selected from the following CORE Courses:

ESE 502: Linear Systems

ESE 503: Stochastic Systems

ESE 511: Solid-State Electronics

ESE 520: Applied Electromagnetics

ESE 528: Communication Systems or ESE 532: Theory of Digital Communication

ESE 545: Computer Architecture

ESE 554: Computational Models for Computer Engineers

ESE 555: Advanced VLSI Circuit Design

4. ESE 597, ESE 599, ESE 697, ESE 698 and ESE 699 are not counted as regular courses in (3). Topics course, ESE 670, can be counted only once as a regular course.

5. At least one (maximum three) credit of ESE 597. In exceptional circumstances, the Graduate Program Director can approve a replacement of ESE 597 credit by ESE 599, ESE 699 or ESE 698.

Credits for ESE 597 can only be applied toward the electrical engineering degree if the following requirements are satisfied:

Prior approval from the Graduate Program Director based on the student submitting a proposal and securing an advisor in the ECE Department as well as a contact person at the company involved. Approval will only be granted if it can be demonstrated that the faculty advisor will be kept in close touch with work on the project. To this end, practicum not in the local geographic area will be discouraged.

To obtain satisfactory credit the faculty advisor must verify that a substantial engineering project was undertaken and completed. This will be based on his close contact during the entire period of the project with the student and the contact person and upon reviewing a mandatory written report submitted by the student at the project's completion. The faculty advisor will determine the final grade for the course.

CPT will be approved one time and can be taken during the summer or the academic year.  
**Extensions of CPT will not be granted.**

6. Students must satisfactorily complete a thesis.

*A candidate for the master's degree may petition to transfer a maximum of 12 graduate credits from another institution towards the master's degree requirements. Students transferring from non-matriculated status are also limited to a maximum of 12 credits for the master's degree.*

### **III. Ph.D. DEGREE in *Electrical Engineering***

#### **A. Major and minor area requirements**

1. Major area requirement is satisfied by taking minimum of three (3) courses from a selected major area with minimum GPA of 3.5. See preapproved lists of courses for each area.
2. Minor area requirement is satisfied by taking courses from other areas (different from the selected major area) with minimum GPA of 3.0. Students with BS degree (G3) are required to take two (2) courses from other areas while students with MS degree (G4) are required to take one (1) course.

#### **B. Course Requirements**

1. A minimum of six (6) regular courses beyond the MS degree or 14 regular courses beyond the BS degree (including courses taken to satisfy major and minor requirements). The choice must have the prior approval of the designated faculty academic advisor. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.
2. ESE 697 Practicum in Teaching (3 credits) is required to satisfy the teaching requirement. Students must be G-5 status in order to take this course, i.e. be advanced to candidacy.
3. The courses ESE 597, ESE 598, ESE 599, ESE 698, and ESE 699 are not counted as regular courses.
4. Courses presented under the title ESE 670 Topics in Electrical Sciences that have different subject matters, and are offered as formal lecture courses, are considered different regular courses but may not be counted more than twice.

#### **C. Advancement to Candidacy**

After successfully completing all major/minor/course requirements (except ESE 697) the student is eligible to be recommended for advancement to candidacy. This status is conferred by the Dean of the Graduate School upon recommendation from the chairperson of the Department. Departmental policy requires that all G3 classified doctoral students Advance to Candidacy within 2.5 years from his/her academic start at Stony Brook University. All G4 classified doctoral students are required to Advance to Candidacy within 1.5 years from his/her academic start.

#### **D. Preliminary Examination**

A student must pass the Preliminary Examination not more than 1.5 years after advancement to candidacy. Both a thesis topic and the thesis background area are emphasized. Students must pass the Preliminary Examination at least ONE year prior to their Defense. See preliminary examination requirements for details.

#### **E. Dissertation**

The most important requirement for the Ph.D. degree is the completion of a dissertation, which must be an original scholarly investigation. The dissertation must represent a significant contribution to the scientific and engineering literature, and its quality must be compatible with the publication standards of appropriate and reputable scholarly journals.

## **F. Approval and Defense of Dissertation**

The dissertation must be orally defended before a dissertation examination committee (normally in three - four years after satisfying the major and the minor requirements). The candidate must obtain approval of the dissertation from this committee. The committee must have a minimum of four members (at least three of whom are faculty members from the department), including the research advisor, at least one person from outside the department, and a committee chair. (Neither the research advisor nor the outside member may serve as the chair.) On the basis of the recommendation of this committee, the Dean of Engineering and Applied Sciences will recommend acceptance or rejection of the dissertation to the Dean of the Graduate School. All requirements for the degree will have been satisfied upon the successful defense of the dissertation.

## **G. Residency Requirement**

The student must complete two consecutive semesters of full-time graduate study. Full-time study is 12 credits per semester until 24 graduate credits have been earned. After 24 graduate credits have been earned, the student may take only nine credits per semester for full time status.

## **POLICIES:** *Electrical Engineering Major*

### **1. MAJOR REQUIREMENT:**

The minimum GPA of 3.5 for a minimum of three (3) courses from preapproved lists corresponding to the areas:

1. Communications and Signal Processing
2. Semiconductor Devices and Quantum Electronics
3. Circuits and VLSI

A student without an electrical or computer engineering background may petition the graduate committee in writing to take a non-traditional major written exam in a novel subject of interest to electrical and computer engineering. Petitions must include a letter of support from their intended Ph.D. advisor.

**2. MINOR REQUIREMENT:** adds an extra degree of breadth to the student's Ph.D. education, and makes possible interdisciplinary research.

The minimum GPA of 3.0 for a minimum of one (MS students) or two (BS students) courses selected from the lists corresponding to any of the areas different from the chosen major area:

1. Communications and Signal Processing
2. Semiconductor Devices and Quantum Electronics
3. Circuits and VLSI
4. Computer Engineering

### **3. ADVANCEMENT TO CANDIDACY:**

Departmental policy requires that all **G3** classified doctoral students Advance to Candidacy within **2.5 years** from his/her academic start at Stony Brook University. All **G4** classified doctoral students are required to Advance to Candidacy within **1.5 years** from his/her academic start.

Students may Advance to Candidacy before completing the Preliminary Examination. ALL required course work (except ESE 697) must be satisfactorily completed prior to Advancing to Candidacy. ALL required course work includes:

- Satisfying major and minor requirements
- At least one year residency (two consecutive semesters of full time graduate study)
- Six (6) regular courses beyond the MS degree or fourteen (14) regular courses above the BS degree, the courses ESE 597, ESE 599, ESE 697, ESE 698 and ESE 699 are NOT counted as regular courses
- A GPA of at least 3.0

The student and his/her research advisor will decide when the student is ready to Advance to Candidacy. The Graduate Program Director must approve the request before final approval is decided by the Graduate School.

The student must submit the Request for Advancement to Candidacy to the Department at least ONE full week prior to the start of the semester. If a request is received after the deadline, advancement to candidacy (G5 status) will NOT take effect until the NEXT semester or term. The request form is available on ECE website.

G5 is the final stage of Ph.D. students. To ensure timely completion and high quality of dissertation, in general, Ph.D. students should not change their dissertation advisors at G5 status. However, if for

some special reason, a G5 student has to change his/her advisor, it is **required that the student spends at least two more years on the new research for the dissertation with the new advisor.** Shorter completion time is allowed if the old advisor and the new advisor of the student have reached a written agreement. The formal switching of advisors and the agreement between the old and new advisors (if any) must be documented at the time of switching by the Graduate Program Coordinator to be effective.

This policy is in effect in order to ensure the high quality of our Ph.D. program, and the above rule will be enforced when the Department submits the Defense form to the Graduate School.

#### **4. PRELIMINARY EXAMINATION:**

This is an oral examination. Usually, a student prepares a short written research proposal and gives a 30-minute presentation. After that the floor is open for questioning.

- I. Departmental policy requires that all doctoral students take the Preliminary Oral Examination **within 1.5 years** after advancement to candidacy. ALL required courses (including major/minor courses) must also be satisfactorily completed.
- II. The student and his/her research advisor will together propose an examination committee and a date and time for the examination. They must obtain prior agreement to serve from each proposed member. The Graduate Program Director must approve the committee and then recommend to the Dean of the Graduate School that said committee be appointed.
- III. The examining Preliminary Committee shall consist of at least four members, including the student's research advisor. If the advisor is from outside of the ECE department, a co-advisor from the department must be chosen. Of the four, at least two must be full-time faculty members from the ECE Department and at least one must be from outside the department or university. If member is from outside of the university, the student must provide a curriculum vitae.
- IV. The student must submit the proposed committee to the Department at least **THREE** full weeks prior to the proposed examination date.
- V. At least **five** full days prior to the examination date, the student must deliver to each committee member a readable copy of the following:
  1. Formal research proposal – proposal shall include a brief abstract (not to exceed 200 words), a table of contents, and within a standard format of Introduction, Discussion, etc., an outline of the related state-of-the-art, the specific research goals, methods to be followed, preliminary results (if any), prospects for success, alternatives if success is in doubt, etc. Except where results already obtained are sufficient to justify a near-final draft of one or more papers to be submitted or of whole chapters in the final thesis, the formal proposal should be at least 50 pages.
  2. A brief Vita to include name, education, date of admission to ECE graduate program, and area working on. Part-time students and any students who have interrupted their graduate studies to seek full-time employment must include their record of employment throughout the period. Any awards, significant publications, or other professional recognition should also be included.

**Students must pass the Preliminary Exam at least ONE year prior to their Defense.**

### **5. DISSERTATION DEFENSE:**

This is an oral examination. An examination committee will consist of at least four members; three members must be Electrical & Computer Engineering faculty, and at least one member must be from outside of the Department. The student must submit to the committee members a copy of his/her dissertation with sufficient time before the actual Defense, to permit careful reading. The Defense consists typically of a 45-minute presentation followed by questions.

Note: At least FIVE weeks prior to the Defense date, the student must electronically submit committee member names, title of Defense, date, time and place of Defense, along with an Abstract (350 words or less) to the Department.

Note: The Dissertation Defense Exam should be scheduled at least FOUR weeks prior to the Graduation date.

**Important: A student must be Advanced to Candidacy (G5) and pass the Preliminary Exam at least ONE year prior to the Defense.**

# *COMPUTER ENGINEERING*

# COMPUTER ENGINEERING DEGREE REQUIREMENTS

Admission to the M.S. program in Computer Engineering requires the student to have completed a Bachelor degree in Computer Engineering or Computer Science. Students with a Bachelor degree in Electrical Engineering could also be admitted if they have taken or will take the following courses or their equivalent:

- ESE 345: Computer Architecture
- ESE 380: Embedded Microprocessor Systems Design I
- ESE 333: Real-Time Operating Systems

## I. M.S. in *Computer Engineering* NON-THESIS OPTION

**Please Note: Full-time M.S. students typically finish the Master's Degree in three (3) semesters.**

1. At least 30 graduate credits with a cumulative and departmental grade point average of 3.0 or better. Among these 30 credits, up to six credits may be ESE 597, ESE 599, or ESE 698. Only 3 credits of ESE 698 may be used. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.

2. At least one (1) course from each of the following sub-areas:

Hardware: ESE/CSE 536 Switching and Routing in Parallel and Distributed Systems  
ESE 545 Computer Architecture  
ESE 565 Parallel Processing Architectures  
ESE 566 Hardware-Software Co-Design of Embedded Systems  
ESE 580 Microprocessor-Based Systems I  
ESE 581 Microprocessor-Based Systems II

Networking: ESE 505 Wireless Network  
ESE 506 Wireless Networking and Mobile Computing  
ESE 546 Networking Algorithms and Analysis  
ESE 548 Local & Wide Area Networks  
ESE 550 Network Management and Planning

CAD and VLSI: ESE 530 Computer Aided Design  
ESE 549 Advanced VLSI System Testing  
ESE 555 Advanced VLSI Circuit Design  
ESE 556 VLSI Physical and Logic Design Automation  
ESE 575 Advanced VLSI Signal Processing Architecture

Theory: ESE 554 Computational Models for Computer Engineers  
CSE 548/AMS 542 Analysis of Algorithms

Software: ESE 568 Computer and Robot Vision  
ESE 588 Pattern Recognition  
CSE 504 Compiler Design  
CSE 506 Operating Systems  
CSE 526 Principles of Programming Languages  
CSE 533 Network Programming  
CSE 548/AMS 542 Analysis of Algorithms

3. At least three (3) additional regular lecture based courses. ESE 597, ESE 599, ESE 697, ESE 698

and ESE 699 are not counted as regular courses. Topics course, ESE 670, can be counted only once as a regular course.

4. At least one (maximum three) credit of ESE 597. In exceptional circumstances, the Graduate Program Director can approve a replacement of ESE 597 credit by ESE 599, ESE 699 or ESE 698 one.

Credits for ESE 597 can only be applied toward the electrical engineering degree if the following requirements are satisfied:

Prior approval from the Graduate Program Director based on the student submitting a proposal and securing an advisor in the ECE Department as well as a contact person at the company involved. Approval will only be granted if it can be demonstrated that the faculty advisor will be kept in close touch with work on the project. To this end, practicum not in the local geographic area will be discouraged.

To obtain satisfactory credit the faculty advisor must verify that a substantial engineering project was undertaken and completed. This will be based on his close contact during the entire period of the project with the student and the contact person and upon reviewing a mandatory written report submitted by the student at the project's completion. The faculty advisor will determine the final grade for the course.

CPT will be approved one time and can be taken during the summer or the academic year.  
**Extensions of CPT will not be granted.**

*A candidate for the master's degree may petition to transfer a maximum of 12 graduate credits from another institution towards the master's degree requirements. Students transferring from non-matriculated status are also limited to a maximum of 12 credits for the master's degree.*

## II. M.S. in **Computer Engineering** THESIS OPTION.

**Please Note: Full-time M.S. students typically finish the Master's Degree in four (4) semesters.**

1. Students must inform the Department in writing at the end of their first semester if they would like to choose the M.S. Thesis Option.

2. At least 30 graduate credits with a cumulative and departmental grade point average of 3.0 or better. Among these 30 credits, at least six credits of ESE 599, with a maximum of 12 credits total being taken from ESE 599, ESE 597, or ESE 698. Only three credits of 698 can be used. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.

3. At least one (1) course from each of the following sub-areas:

Hardware:                   ESE/CSE 536 Switching and Routing in Parallel and Distributed Systems  
                                  ESE 545 Computer Architecture  
                                  ESE 565 Parallel Processing Architectures  
                                  ESE 566 Hardware-Software Co-Design of Embedded Systems  
                                  ESE 580 Microprocessor-Based Systems I  
                                  ESE 581 Microprocessor-Based Systems II

Networking:                ESE 505 Wireless Network  
                                  ESE 506 Wireless Networking and Mobile Computing  
                                  ESE 546 Networking Algorithms and Analysis  
                                  ESE 548 Local & Wide Area Networks  
                                  ESE 550 Network Management and Planning

CAD and VLSI:            ESE 530 Computer Aided Design  
                                  ESE 549 Advanced VLSI System Testing

ESE 555 Advanced VLSI Circuit Design  
ESE 556 VLSI Physical and Logic Design Automation  
ESE 575 Advanced VLSI Signal Processing Architecture

Theory: ESE 554 Computational Models for Computer Engineers  
CSE 548/AMS 542 Analysis of Algorithms

Software: ESE 568 Computer and Robot Vision  
ESE 588 Pattern Recognition  
CSE 504 Compiler Design  
CSE 506 Operating Systems  
CSE 526 Principles of Programming Languages  
CSE 533 Network Programming  
CSE 548/AMS 542 Analysis of Algorithms

4. At least one (1) additional regular lecture based course. ESE 597, ESE 599, ESE 697, ESE 698 and ESE 699 are not counted as regular courses. Topics course, ESE 670, can be counted only once as a regular course.

5. At least one (maximum three) credit of ESE 597. In exceptional circumstances, the Graduate Program Director can approve a replacement of ESE 597 credit by ESE 599, ESE 699 or ESE 698 one.

Credits for ESE 597 can only be applied toward the electrical engineering degree if the following requirements are satisfied:

Prior approval from the Graduate Program Director based on the student submitting a proposal and securing an advisor in the ECE Department as well as a contact person at the company involved. Approval will only be granted if it can be demonstrated that the faculty advisor will be kept in close touch with work on the project. To this end, practicum not in the local geographic area will be discouraged.

To obtain satisfactory credit the faculty advisor must verify that a substantial engineering project was undertaken and completed. This will be based on his close contact during the entire period of the project with the student and the contact person and upon reviewing a mandatory written report submitted by the student at the project's completion. The faculty advisor will determine the final grade for the course.

CPT will be approved one time and can be taken during the summer or the academic year.

**Extensions of CPT will not be granted.**

6. Students must satisfactorily complete a thesis (see graduate student guide for details).

*A candidate for the master's degree may petition to transfer a maximum of 12 graduate credits from another institution towards the master's degree requirements. Students transferring from non-matriculated status are also limited to a maximum of 12 credits for the master's degree.*

### **III. Ph.D. DEGREE in *Computer Engineering***

#### **A. Major and minor area requirements**

1. Major area requirement is satisfied by taking minimum of three (3) courses from a selected major area with minimum GPA of 3.5. See preapproved lists of courses for each area.
2. Minor area requirement is satisfied by taking courses from other areas (different from the selected major area) with minimum GPA of 3.0. Students with BS degree (G3) are required to take two (2) courses from other areas while students with MS degree (G4) are required to take one (1) course.

#### **B. Course Requirements**

1. A minimum of six (6) regular courses beyond the MS degree or 14 regular courses beyond the BS degree (including courses taken to satisfy major and minor requirements). The choice must have the prior approval of the designated faculty academic advisor. Any non-ESE course will need prior approval given by the Graduate Program Director before a student can register.
2. ESE 697 Practicum in Teaching (3 credits) is required to satisfy the teaching requirement. Students must be G-5 status in order to take this course, i.e. be advanced to candidacy.
3. The courses ESE 597, ESE 598, ESE 599, ESE 698, and ESE 699 are not counted as regular courses.
4. Courses presented under the title ESE 670 Topics in Electrical Sciences that have different subject matters, and are offered as formal lecture courses, are considered different regular courses but may not be counted more than twice.

#### **C. Advancement to Candidacy**

After successfully completing all major/minor/course requirements (except ESE 697) the student is eligible to be recommended for advancement to candidacy. This status is conferred by the Dean of the Graduate School upon recommendation from the chairperson of the Department. Departmental policy requires that all G3 classified doctoral students Advance to Candidacy within 2.5 years from his/her academic start at Stony Brook University. All G4 classified doctoral students are required to Advance to Candidacy within 1.5 years from his/her academic start.

#### **D. Preliminary Examination**

A student must pass the Preliminary Examination not more than 1.5 years after advancement to candidacy. Both a thesis topic and the thesis background area are emphasized. Students must pass the Preliminary Examination at least ONE year prior to their Defense. See preliminary examination requirements for details.

#### **E. Dissertation**

The most important requirement for the Ph.D. degree is the completion of a dissertation, which must be an original scholarly investigation. The dissertation must represent a significant contribution to the scientific and engineering literature, and its quality must be compatible with the publication standards of appropriate and reputable scholarly journals.

## **F. Approval and Defense of Dissertation**

The dissertation must be orally defended before a dissertation examination committee (normally in three - four years after satisfying the major and the minor requirements). The candidate must obtain approval of the dissertation from this committee. The committee must have a minimum of four members (at least three of whom are faculty members from the department), including the research advisor, at least one person from outside the department, and a committee chair. (Neither the research advisor nor the outside member may serve as the chair). On the basis of the recommendation of this committee, the Dean of Engineering and Applied Sciences will recommend acceptance or rejection of the dissertation to the dean of the Graduate School. All requirements for the degree will have been satisfied upon the successful defense of the dissertation.

## **G. Residency Requirement**

The student must complete two consecutive semesters of full-time graduate study. Full-time study is 12 credits per semester until 24 graduate credits have been earned. After 24 graduate credits have been earned, the student may take only nine credits per semester for fulltime status.

## **POLICIES: *Computer Engineering Major***

### **1. MAJOR REQUIREMENT:**

The minimum GPA of 3.5 for a minimum of three (3) courses from preapproved lists corresponding to the areas:

1. Communications and Signal Processing;
2. Computer Engineering;
3. Circuits and VLSI.

A student without electrical or computer engineering background may petition the graduate committee in writing to take a non-traditional major written exam in a novel subject of interest to electrical and computer engineering. Petitions must include a letter of support from their intended Ph.D. advisor.

**2. MINOR REQUIREMENT:** adds an extra degree of breadth to the student's Ph.D. education, and makes possible interdisciplinary research.

The minimum GPA of 3.0 for a minimum of one (MS students) or two (BS students) courses selected from the lists corresponding to any of the areas different from the chosen major area:

1. Communications and Signal Processing
2. Semiconductor Devices and Quantum Electronics
3. Circuits and VLSI
4. Computer Engineering

### **3. ADVANCEMENT TO CANDIDACY:**

Departmental policy requires that all **G3** classified doctoral students Advance to Candidacy within **2.5 years** from his/her academic start at Stony Brook University. All **G4** classified doctoral students are required to Advance to Candidacy within **1.5 years** from his/her academic start.

Students may Advance to Candidacy before completing the Preliminary Examination. ALL required course work (except ESE 697) must be satisfactorily completed prior to Advancing to Candidacy. ALL required course work includes:

- Satisfying major and minor requirements
- At least one year residency (two consecutive semesters of full time graduate study)
- Six (6) regular courses beyond the MS degree or fourteen (14) regular courses above the BS degree. The courses ESE 597, ESE 599, ESE 697, ESE 698 and ESE 699 are NOT counted as regular courses.
- A GPA of at least 3.0

The student and his/her research advisor will decide when the student is ready to Advance to Candidacy. The Graduate Program Director must approve the request before final approval is decided by the Graduate School.

The student must submit the Request for Advancement to Candidacy to the Department at least ONE full week prior to the start of the semester. If a request is received after the deadline, advancement to candidacy (G5 status) will NOT take effect until the NEXT semester or term. Request form available on ECE website.

G5 is the final stage of Ph.D. students. To ensure timely completion and high quality of dissertation, in general, Ph.D. students should not change their dissertation advisors at G5 status. However, if for

some special reason, a G5 student has to change his/her advisor, it is **required that the student spends at least two more years on the new research for the dissertation with the new advisor.** Shorter completion time is allowed if the old advisor and the new advisor of the student have reached a written agreement. The formal switching of advisors and the agreement between the old and new advisors (if any) must be documented at the time of switching by the Graduate Program Coordinator to be effective.

This policy is in effect in order to ensure the high quality of our Ph.D. program, and the above rule will be enforced when the Department submits the Defense form to the Graduate School.

#### **4. PRELIMINARY EXAMINATION:**

This is an oral examination. Usually, a student prepares a short written research proposal and gives a 30-minute presentation. After that the floor is open for questioning.

- I. Departmental policy requires that all doctoral students take the Preliminary Oral Examination **within 1.5 years** after advancement to candidacy. ALL required courses (including major/minor courses) must also be satisfactorily completed.
- II. The student and his/her research advisor will together propose an examination committee and a date and time for the examination. They must obtain prior agreement to serve from each proposed member. The Graduate Program Director must approve the committee and then recommend to the Dean of the Graduate School that said committee be appointed.
- III. The examining Preliminary Committee shall consist of at least four members, including the student's research advisor. If the advisor is from outside of the ECE department, a co-advisor from the department must be chosen. Of the four, at least two must be full-time faculty members from the ECE Department and at least one must be from outside the Department or university. If member is from outside of the university, the student must provide a curriculum vitae.
- IV. The student must submit the proposed committee to the Department at least **THREE** full weeks prior to the proposed examination date.
- V. At least **five** full days prior to the examination date, the student must deliver to each committee member a readable copy of the following:
  3. Formal research proposal – proposal shall include a brief abstract (not to exceed 200 words), a table of contents, and within a standard format of Introduction, Discussion, etc., an outline of the related state-of-the-art, the specific research goals, methods to be followed, preliminary results (if any), prospects for success, alternatives if success is in doubt, etc. Except where results already obtained are sufficient to justify a near-final draft of one or more papers to be submitted or of whole chapters in the final thesis, the formal proposal should be at least 50 pages.
  4. A brief Vita to include name, education, date of admission to ECE graduate program, and area working on. Part-time students and any students who have interrupted their graduate studies to seek full-time employment must include their record of employment throughout the period. Any awards, significant publications, or other professional recognition should also be included.

**Students must pass the Preliminary Exam at least ONE year prior to their Defense.**

### **5. DISSERTATION DEFENSE:**

This is an oral examination. An examination committee will consist of at least four members; three members must be Electrical & Computer Engineering faculty, and at least one member must be from outside the department. The student must submit to the committee members a copy of his/her dissertation with sufficient time before the actual Defense, to permit careful reading. The Defense consists typically of a 45-minute presentation followed by questions.

Note: At least FIVE weeks prior to the Defense date, the student must electronically submit committee member names, title of Defense, date, time and place of Defense, along with an Abstract (350 words or less) to the Department.

Note: The Dissertation Defense Exam should be scheduled at least FOUR weeks prior to the Graduation date.

**Important: A student must be Advanced to Candidacy (G5) and pass the Preliminary Exam at least ONE year prior to the Defense.**

## ***Area Courses for PhD major/minor requirements***

### **Communications and Signal Processing**

1. ESE 503: Stochastic Systems
2. ESE 505: Wireless Communications/ESE 522: Fiber Optic Systems
3. ESE 528: Communication Systems/ESE 532: Theory of Digital Communications
4. ESE 531: Detection and Estimation Theory
5. ESE 546: Networking Algorithms and Analysis
6. ESE 547: Digital Signal Processing
7. ESE 558: Digital Image Processing

### **Computer Engineering**

1. ESE 506: Wireless Networking and Mobile Computing or ESE 537: Mobile Sensing Systems & Application or ESE 543: Mobile Cloud Computing
2. ESE 507: Advanced Digital System Design & Generation or ESE 556: VLSI Physical and Logic Level Design Automation
3. ESE 536 (CSE 626): Switching and Routing in Parallel and Distributed Systems
4. ESE 545: Computer Architecture
5. ESE 554: Computational Models for Computer Engineers
6. ESE 566: Hardware-Software Co-Design of Embedded Systems
7. CSE 548 (AMS 542): Analysis of Algorithms

### **Semiconductor Devices and Quantum Electronics**

1. ESE 511: Solid States Electronics
2. ESE 513: Introduction to Photovoltaics
3. ESE 515: Quantum Electronics I
4. ESE 516: Integrated Electronic Devices and Circuits I
5. ESE 517: Integrated Electronic Devices and Circuits II
6. ESE 519: Semiconductor Lasers and Photodetectors
7. ESE 538: Nanoelectronics

### **Circuits and VLSI**

1. ESE 501: System Specification and Modeling
2. ESE 507: Advanced Digital System Design & Generation
3. ESE 516: Integrated Electronic Devices and Circuits I
4. ESE 517: Integrated Electronic Devices and Circuits II or ESE 585: Nanoscale Integrated Circuit Design
5. ESE 555: Advanced VLSI Systems Design
6. ESE 556: VLSI Physical and Logic Design Automation
7. ESE 566: Hardware-Software co-Design or ESE 575: Advanced VLSI Signal Processing Architecture or ESE 549: Advanced VLSI System Testing

## **POLICIES REGARDING RA/TA/GA SUPPORT AND TUITION SCHORLARSHIP/TEACHING ASSISTANT DUTIES AND EVALUATION**

1. Full-time graduate students are eligible for research assistant (RA) positions. Individual faculty should be contacted on the availability of RA positions.
2. State lines - Teaching Assistantships (TA) and Graduate Assistantships (GA) - are used by the Department to help fulfill its undergraduate academic mission and at the same time to support graduate students, who are making satisfactory progress towards a Ph.D. degree.
3. Tuition scholarship may be awarded to an RA/TA/GA if the Department budget permits. Preference will be given to Ph.D. students. If a student does not pass the Preliminary Oral Examination within 1.5 years after advancing to candidacy, the student may not receive the tuition scholarship.

**Note: In order to receive a Graduate School Tuition Scholarship as a G5 student, the student must have been fully supported as a TA/GA/RA with a Department tuition scholarship for the academic year prior to advancing to candidacy. Otherwise the student will be liable for his/her own tuition costs.**

4. Because of the limited TA positions, the awarding of a TA will be according to the following guidelines:

State (TA) support is limited to four years, which includes periods when a student is only partially supported during MS or Ph.D.

Except for new incoming supported students, state support will generally be limited to 1/2 TA, with the priority given to eligible students awarded at least 1/2 RA by their faculty advisor.

To be eligible for second-year support, the student must satisfy major requirements and must have a GPA of at least 3.0. The student must also have a research advisor in the Electrical & Computer Engineering Department. To be eligible in subsequent years, the student must be making satisfactory progress in taking the courses for the Minor Area. Additionally, the student must have a grade of 'S' on all previous research courses and assistantship assignments.

The remaining TA lines will be allocated on a competitive basis, based on the academic merit of the candidate. The guiding principle for the Committee will be the need to ensure continuity and completion of a successful Ph.D. project, as well as to help the new faculty to establish their research program.

5. Academic dishonesty or failure to meet assigned TA or GA responsibilities may result in loss of any support from the program.

Each semester, all Teaching Assistants must be on campus and in contact with the faculty member of the course to which he/she has been assigned by the FIRST day of classes. If a TA does not meet this requirement, they will be subject to having their stipend and tuition scholarship rescinded.

## TEACHING ASSISTANT DUTIES AND EVALUATION

All TAs are expected to behave professionally and take their teaching responsibilities seriously.

All TAs must be on campus and in contact with the faculty member of their TA Assignment by the first day of classes or they are subject to having their stipend and tuition scholarship rescinded.

- A full-time TA is expected to work up to 20 hours a week. However, it is understood that this load is not distributed evenly during the semester. In the first few weeks, the load is usually much less than 20 hours. The load tends to increase towards the semesters end.
- If it becomes apparent that the average load constantly exceeds 20 hours per week, the student should discuss the problem with the supervisor.
- A TA must be familiar with the concepts and course material. TAs must prepare designated material and understand and assist with the everyday operation of the course. TAs are required to attend the lectures, unless excused by their supervisor.
- A TA must understand and follow course standards set by the supervisor.
- A TA must be reliable. Perfect attendance is expected in classes, assigned recitation, laboratory and help sessions, office hours, TA meetings, and proctoring. If a TA can't meet a responsibility, they must inform their supervisor and where appropriate, must find a substitute acceptable to their supervisor.
- A TA must be prepared for all recitations, labs, meetings, etc.
- A TA must treat all students with courtesy regardless of gender, race, ethnic background, etc.
- A TA must not initiate or maintain inappropriate relationships with students. These include romantic, sexual, or financial. The University prohibits such abuses of power.
- A TA must have proficiency in English sufficient for communication with students.
- A TA must grade consistently and fairly, according to the course standards determined by the course instructor.
- At the end of each semester, a TA must assist the supervisor as long as necessary to help the supervisor with grading exams, labs, and other assignments.
- A TA must complete grading and other assigned duties promptly based upon the standards and guidelines set by their supervisor.
- A TA must take the designated graduate teaching assistant training classes.

If a TA fails to satisfy any of the above responsibilities, their supervisor will meet with them to rectify any problems. If problems persist, the TA must meet with the Director of Graduate Studies and further actions may be taken.

**TA Evaluation:** TA's will be evaluated each semester by the supervisor as follows:

- Excellent, well-above expectations
- Very good
- Good, meets expectations
- Below expectations
- Unsatisfactory, well-below expectations

Teaching Assistants who perform below expectations may see their support terminated in the following semester.

## ***CURRICULAR PRACTICAL TRAINING***

CPT must be in the student's major area of study and MUST count towards completion of the degree requirements. The master's degree requires 30 credits. Students must plan their CPT credits along with their other course credits accordingly.

Master's students are required to take ONE credit of CPT to fulfill the master's degree requirements. CPT can be taken only ONE time. Students can take CPT during the academic year OR the summer. In exceptional circumstances, the Graduate Program Director can approve a replacement for this requirement by substituting one credit of ESE 599, ESE 699 or ESE 698.

### **ESE Department Requirements for CPT Approval**

- **Students should consult with an International Student Adviser to confirm that they are eligible for CPT. If eligible, please submit the following to the Graduate Program Coordinator at least two weeks prior to the start of the internship:**
  - **CPT Application instructions and application are located on the Visa & Immigration Services website. (AccessVIS)**
  - **Letter of Offer from the internship supervisor to the department, which states the start & end dates of the internship, hours per week, and specific CPT job duties. This letter must be on original company letterhead.**
  - **E-Mail the Graduate Program Coordinator, stating how the CPT is integral to your program of study.**

### **Registration Requirement**

Students must register for ESE 597 (variable credit). ESE 597 is a non-regular course. Register under your advisor's section number. If you do not have an advisor, you should register under the section number of the Graduate Program Director.

### **Requirements Upon Completion of CPT**

The internship supervisor must inform the student's advisor or Graduate Program Director via e-mail that the student has satisfactorily fulfilled the CPT requirements.

Student must submit a CPT report (1-2 pages) to their advisor or the Graduate Program Director **PRIOR** to the beginning of the next semester. A grade of "S" or "U" will then be assigned.

## FACILITIES

The Department operates laboratories for both teaching and research:

**The Computer-Aided Design Laboratory** offers access to large assortment of software tools used to analyze, model, simulate, and better understand various engineering concepts. The lab comprises 40 Dell PC's, that are networked via switched Ethernet to a Dell file server.

**The Computer Vision Laboratory** has a network of PC's, digital imaging hardware, and custom built Computer Vision Systems for experimental research in 3D vision and digital image processing.

**The COSINE Laboratory** supports the research efforts of faculty members and graduate and undergraduate students in the areas of signal processing, communications, and networking. Current and recent research projects involve Bayesian signal processing, inference, Monte Carlo signal processing, signal modeling, machine learning, deep networks, signal processing over networks, graph signal processing, sensor signal processing, positioning and navigation, biomedical signal processing, wireless networks, radio-frequency identification, the Internet of Things, computer networking, data transmission, multiple-access systems, scheduling, network performance evaluation, grid computing, information theory, and image processing.

**The Digital Signal Processing Laboratory** is involved in digital signal processing architectures and hardware and software research. The laboratory has extensive list of relevant software and hardware tools.

**The Fluorescence Detection Laboratory** is involved in the design and development as well as implementation and testing of various instruments for Life Sciences. Research areas include laser induced fluorescence detection, single photon counting techniques, fast data acquisition and transfer, design and development of analog and digital integrated circuits, signal processing, capillary electrophoresis phenomena, DNA sequencing, and microfluidics.

**The Graduate Computing Laboratory** has extensive computational capabilities to support student's research and studies. Industry standard packages such as Cadence tools, Synopsys, Matlab, and many others are available.

**The Hardware Generation and Optimization (HGO) Laboratory** is dedicated to the design and optimization of digital systems, with a focus on field-programmable gate arrays (FPGAs). The lab is equipped with FPGA development systems (furnished in part through donations from Xilinx, Altera, and Intel), with all related tools.

**The High Performance Computing and Networking Research Laboratory** is equipped to conduct research in the broad area of networking and parallel/distributed computing with emphasis on wireless/mobile networks, cloud computing, data center networks, optical networks, high-speed networks, interconnection networks and multicast communication.

**The Integrated Microsystems Laboratory** focuses on advancing the performance of CMOS IC at analog sensor interfaces. We investigate design of miniature, low-power, highly accurate sensing microsystems, that have a significant and pervasive impact on a large number of applications, ranging from new generation of biomedical devices for personal health monitors, hearing aids or implantable neural prostheses to communication devices and radiation detectors.

**The Nanoscale Circuits and Systems (NanoCAS) Laboratory** focuses on developing design methodologies for high performance as well as energy efficient integrated circuits with a variety of applications ranging from future processors to ultra-low power Internet-of-things (IoT) based devices. The NanoCAS Lab is equipped with a high performance processing and storage server, workstations, and all necessary EDA tools for modeling, design, and analysis.

**The Mixed-Domain Embedded System Laboratory** is equipped for research in the broad area of electronic system design and design automation. Current research projects involve design automation for mixed analog-digital systems and embedded systems for multimedia, sensor network applications and emerging technologies.

**The Mobile Computing and Applications Laboratory** conducts research in mobile computing systems, especially those using sensing devices for various applications in location based services, Internet-of-Things, and healthcare. The laboratory has various latest mobile and embedded devices, and access to a cloud computing facility.

**The Mobile Systems Design Laboratory** conducts research in the broad areas of VLSI system designs for signal processing, communication, and heterogeneous mobile sensors. The laboratory is equipped for design and simulation of complex hardware and software systems.

**The Optoelectronics Laboratory** possesses the infrastructure for molecular beam epitaxial semiconductor heterostructure growth, advanced material characterization as well as fabrication (clean room) and sophisticated characterization and modeling of optoelectronics devices. The recent research projects include design and development of the novel infrared lasers, light emitting diodes, photodetectors and modulators. The laboratory is actively working on metamorphic epitaxial growth techniques to develop the new class of narrow and ultra-low bandgap alloys and superlattices for long-wave infrared photodetector and other applications.

**The Ultra-High-Speed Computing Laboratory** conducts research in high performance energy-efficient flux quantum computing and cybersecurity. It is equipped with powerful computing, networking, and storage facilities and advanced CAD tools for superconductor circuit design.

**The Fiber Optic Sensors Laboratory (FOSL)** - Research emphasis is on the development and fabrication of novel fiber optic systems for very diverse applications ranging from aerospace to biomedical. Research work has been supported by NSF, NASA, NIH and various state and industrial partners. Some of the current research projects include development of capillary waveguide based biosensors for detection of pathogens in a marine environment, laser debridement, cavity sensors for flight control surfaces, and photonic power conversion for mobile platforms. The laboratory is equipped with various capabilities for optical and electronic diagnosis. These include a fiber optic fusion splicer, fiber polisher, diamond saw, optical microscope, optical spectral analyzer, single photon-counting systems, a high speed digital autocorrelator and various laser sources. Additionally, the laboratory has the facilities for designing and fabricating printed circuit boards and fabricating optical and electronic sub-systems.

**The Wireless and Networking Systems Laboratory** conducts research in the area of wireless networking and mobile computing. The lab has extensive computing capabilities, a set of crossbow sensors, professional sensor test bed development kit, and other equipment for network and system research.

## **FACULTY**

Faculty with research interest in computer engineering are underlined.

### **Distinguished Professors**

***Belenky, Gregory***, Doctor of Physical and Mathematical Sciences, 1979, Institute of Physics, Baku, USSR: Design, manufacturing, and characterization of optoelectronic and microelectronic semiconductor devices; physics of semiconductors and semiconductor devices.

***Djuric, Petar M.***, Chairperson, Ph.D., 1990, University of Rhode Island: Signal analysis, modeling and processing; wireless communications and sensor networks.

***Luryi, Serge***, Ph.D., 1978, University of Toronto, Canada: High speed solid-state electronic and photonic devices, physics and technology.

***Yang, Yuanyuan***, Ph.D., 1992, Johns Hopkins University: Wireless and mobile networks, cloud computing, data center networks, optical networks, high speed networks, parallel and distributed computing systems, multicast communication, high performance computer architecture, and computer algorithms.

### **Professors**

***Doboli, Alex***, Ph.D., 2000, University of Cincinnati: VLSI CAD and design, synthesis and simulation of mixed analog-digital systems, hardware/software co-design of embedded systems, and high-level synthesis of digital circuits.

***Hong, Sangjin***, Ph.D., 1999, University of Michigan: Low-power VLSI design of multimedia wireless communications and digital signal processing systems, including SOC design methodology and optimization.

***Parekh, Jayant P.***, Ph.D., 1971, Polytechnic Institute of Brooklyn: Microwave acoustics; microwave magnetics; microwave electronics; microcomputer applications.

***Robertazzi, Thomas G.***, 1981, Princeton University: Computer networking; grid computing; performance evaluation; parallel processing; e-commerce technology.

***Short, Kenneth L.***, Ph.D., 1973, University at Stony Brook: Digital system design; microprocessors; instrumentation.

***Subbarao, Murali***, Ph.D., 1986, University of Maryland: Computer vision; image processing; pattern recognition.

### **Associate Professors**

***Bugallo, Monica***, Ph.D., 2001, Universidade da Coruna (Spain): Statistical signal processing with the emphasis in the topics of Bayesian analysis, sequential Monte Carlo methods, adaptive filtering, and stochastic optimization.

***Dhadwal, Harbans***, Ph.D., 1980, University of London, England: Laser light scattering; fiber optics; optical signal processing and instrumentation.

***Donetski, Dmitri***, Ph.D., 2000, Stony Brook University: Design and technology of optoelectronic devices and systems including photovoltaic and photoconductive detectors, diode lasers and diode

laser arrays.

**Dorojevets, Mikhail**, Ph.D., 1988 Siberian Division of the USSR Academy of Sciences, Novosibirsk: Computer architectures, systems design.

**Gindi, Gene**, Ph.D., 1981, University of Arizona: Medical image processing; image analysis.

**Gorfinkel, Vera**, Ph.D., 1980, A.F. Ioffe Physical-Technical Institute, St. Petersburg, Russia: Semiconductor devices, including microwave and optoelectronics, DNA sequencing instrumentation, single photon counting techniques.

**Kamoua, Ridha**, Undergraduate Program Director, Ph.D., 1992, University of Michigan: Solid-state devices and circuits; microwave devices and integrated circuits.

**Salman, Emre**, Ph.D. 2009, University of Rochester: Nanoscale integrated circuit design, emerging technologies for future electronic systems, highly heterogeneous integrated systems, digital and mixed-signal circuits.

**Shterengas, Leon**, Graduate Program Director, Ph.D. 2004, Stony Brook University: High power and high speed light emitters, carrier dynamics in nanostructures, nanofabrication and molecular beam epitaxy of semiconductor nanostructures.

**Stanacevic, Milutin**, Ph.D., 2005, Johns Hopkins University: Analog and mixed-signal VLSI integrated circuits and systems; adaptive Microsystems; implantable electronics.

**Tang, K. Wendy**, Ph.D., 1991, University of Rochester: Interconnection networks, parallel computing, and neural networks.

**Wang, Xin**, Ph.D., 2001, Columbia University: Mobile and ubiquitous computing, wireless communications and networks, grid and distributed computing, advanced applications and services over Internet and wireless networks.

#### **Assistant Professors**

**Eisaman, Matthew**, Ph.D., 2006 Harvard University: Photovoltaic devices, especially light trapping nanostructures for improved solar cell efficiency, and spatial variations at the nanoscale.

**Lin, Shan**, Ph.D., 2010, University of Virginia: Cyber physical systems, networked information systems, wireless networks, sensing and control systems.

**Milder, Peter**, Ph.D. 2010, Carnegie –Mellon University: Digital hardware design, generation, and optimization focusing on signal processing, computer vision, and related domains; design for FPGA.

**Westerfeld, David**, Ph.D. 2005, Stony Brook University: Design and characterization of high-performance mid-infrared semiconductor light sources (LEDs and lasers).

**Ye, Fan**, Ph.D., 2004, UCLA: Mobile computing/sensing systems & applications, indoor localization and floor plan reconstruction, internet-of-things and sensor networks.

**Zhao, Yue**, Ph.D., 2011, UCLA: Smart energy systems, renewable energy integration, electricity market, infrastructure security, sensing and signal processing, optimization theory, information theory, communication networks.

# **COURSE LISTING**

## **ESE 501 System Specification and Modeling**

A comprehensive introduction to the field of System-on-Chip design. Introduces basic concepts of digital system modeling and simulation methodologies. Various types of hardware description language (HDL) will be studied, including Verilog, VHDL and System C. Topics include top-down and bottom-up design methodology, specification language syntax and semantics, RTL, behavioral and system-level modeling, and IP core development. Included are three projects on hardware modeling and simulation. Fall, 3 credits, grading ABCF.

## **ESE 502 Linear Systems- CORE COURSE**

Development of transfer matrices and state-space equations from the concepts of linearity, time-invariance, causality and lumpedness. Op-amp circuit implementations, solutions and equivalent state equations. Companion and modal forms, Stability and Lyapunov equations. Controllability, observability, and their applications in minimal realization, state feedback and state estimators. Coprime fraction of transfer functions and their designs in pole-placement and model matching. Both the continuous-time and discrete-time systems will be studied. Fall, 3 credits, grading ABCF.

## **ESE 503 Stochastic Systems – CORE COURSE**

Basic probability concepts and application. Probabilistic bounds, characteristic functions and multivariate distributions. Central limit theorem, normal random variables. Stochastic processes in communications, control and other signal processing systems. Stationarity, ergodicity, correlation functions, spectral densities and transmission properties. Optimum linear filtering, estimation and prediction. Fall, 3 credits, grading ABCF.

## **ESE 504 Performance Evaluation of Communication and Computer Systems**

Advanced queuing models and algorithms for communication and computer systems. Mean value analysis and convolution algorithm. Transient analysis and M/G/1 queue. Models for traffic characterization in broadband integrated networks. Buffer sizing calculations. Bursty and self-similar traffic. Prerequisite: ESE 503 or permission of instructor. Spring, 3 credits, grading ABCF.

## **ESE 505 Wireless Communications**

This course covers first year graduate level material in the area of wireless communications: wireless channels, overview of digital communications and signal processing for wireless communications, voice and data applications, and design basics for wireless modems. Analysis of system issues like resource management and handoff, cellular and wireless LAN systems. Fall and Spring, 3 credits, grading ABCF.

## **ESE 506 Wireless Network**

This course will examine the area of wireless and mobile computing, looking at the unique network protocol challenges and opportunities presented by wireless communications and host or router mobility. The course will give a brief overview of fundamental concepts in mobile wireless systems and mobile computing, it will then cover system and standards issues including second generation circuit switches and third generation packet switched networks, wireless LANs, mobile IP, ad-hoc networks, sensor networks, as well as issues associated with small handheld portable devices and new applications that can exploit mobility and location information. This is followed by several topical studies around recent research publications in mobile computing and wireless networking field. This course will make the system architecture and applications accessible to the electrical engineer. Prerequisite: ESE 505 and ESE 546 or ESE 548 or permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 507 Advanced Digital System Design & Generation**

This course focuses on languages, tools, and abstractions for design and implementation of digital systems. Course material is divided roughly into three categories: Limitations and constraints on modern digital systems; Hardware design abstractions, languages, and tools (including the SystemVerilog hardware description language); and new architectures and paradigms for digital design. Coursework will be primarily project and assignment based; there will also be reading and discussion of published papers in these areas. Students should have experience with hardware description languages (VHDL, Verilog, or SystemVerilog,) and software (C, C++ or Java). Fall, 3 credits, grading ABCF.

### **ESE 508 Analytical Foundations of Systems Theory**

An exposition of the basic analytical tools for graduate study in systems, circuits, control, and signal processing. Sets and mappings, finite-dimensional linear spaces, metric spaces, Banach spaces, Hilbert spaces. The theory will be developed and exemplified in the context of systems applications such as nonlinear circuits, infinite networks, feedback control, signal restoration via projections, and optimal signal modeling. Spring, 3 credits, grading ABCF.

### **ESE 509 Modern Energy Technologies**

This course will cover a broad array of technologies that are essential to the modern energy industry, specifically focusing on the most contemporary topics and “hot” areas of research, development, and deployment. Students will gain a quantitative understanding of selected energy generation technologies, energy storage technologies, and pollution control technologies. For each of these topics, we will cover the physical principle of operation, as well as the economics and environmental impact. Fall, 3 credits, grading ABCF.

### **ESE 510 Electronic Circuits – This course is only for students in the Optoelectromechanical Systems Eng. Program and cannot be used to fulfill any ESE degree requirement.**

This is a course in the design and analysis of analog circuits, both discrete and integrated. The first part of the course presents basic topics related to circuit analysis: laws, theorems, circuit elements, and transforms. Fundamental semiconductor devices are introduced next. A number of aspects of circuit design beginning with basic device operation through the design of large analog functional blocks including amplifiers, oscillators, and filters are discussed. Fall, 3 credits, grading ABCF.

### **ESE 511 Solid-State Electronics – CORE COURSE**

The course provides an introduction to physics of semiconductor devices. It covers fundamental aspects of semiconductor physics necessary for understanding operation principles and characteristics of semiconductor diodes and transistors. Fall, 3 credits, grading ABCF.

### **ESE 513 Introduction to Photovoltaics**

Introduction to the basic concepts of photovoltaic solar energy conversion, including: 1. The solar resource in the context of global energy demand; 2. The operating principles and theoretical limits of photovoltaic devices; 3. Device fabrication, architecture, and primary challenges and practical limitations for the major technologies and materials used for photovoltaic devices. Students will gain knowledge of: the device physics of solar cells, the operating principles of the major commercial photovoltaics, and a basic understanding of the role of photovoltaics in the context of the global energy system. Pre/co –requisites ESE 231. Spring, 3 credits, grading ABCF.

### **ESE 514 MOS Transistor Modeling**

An overview of the metal-oxide semiconductor (MOS) transistor and its models for circuit analysis. The course is modular in structure. In a common first part, CMOS fabrication, device structure and operation are introduced. Starting from basic concepts of electrostatics, MOS field-effect transistor operation is presented in an intuitive fashion, and no advanced background in solid-state theory is required. Analytical models of increasing complexity and their SPICE implementations are discussed. The second part of the course allows students to focus on their field of preference: Device physics; Digital circuits; Analog circuits. The course includes a project in one of these subtopics. Fall, 3 credits, grading ABCF.

### **ESE 515 Quantum Electronics I**

Physics of microwave and optical lasers. Topics include introduction to laser concepts; quantum theory; classical radiation theory; resonance phenomena in two-level systems: Bloch equations, Kramers-Kronig relations, density matrix; rate equation and amplification; CO<sub>2</sub> lasers; discharge lasers; semiconductor lasers. Fall, 3 credits, grading ABCF.

### **ESE 516, 517 Integrated Electronic Devices and Circuits I and II**

Theory and applications: elements of semiconductor electronics, methods of fabrication, bipolar junction transistors, FET, MOS transistors, diodes, capacitors and resistors. Design techniques for linear digital integrated electronic components and circuits. Discussion of computer-aided design. MSI and LSI. Fall, Spring, 3 credits each semester, grading ABCF.

### **ESE 518 Advanced Design of Low-Noise and Low-Power Analog Circuits**

Design of advanced low-noise and low-power analog and mixed-signal integrated circuits for radiation sensors. Students will learn state-of-the-art circuit techniques for low-noise and low-power amplification and processing of signals from sensors. Examples of circuits are low-noise amplifiers, filters, peak detectors and discriminators. Applications range from medical, to security, safety, industrial measurements and physics research. As a course project, students will develop part of a front-end circuit from transistor level to physical layout using industry-standard CAD tools, and will participate in the experimental characterization of those or similar circuits. At the end of the course the student will own a solid background and the basic instruments to design low-noise and low-power amplifiers and processing circuits. Fall, 3 Credits, grading ABCF.

### **ESE 519 Semiconductor Lasers and Photodectors**

The course provides an introduction to performance, testing and fabrication techniques for semiconductor lasers and photodetectors. The topics include fundamentals of laser and detector operation, devices band diagram, device characteristics, and testing techniques for analog and digital edge emitting and surface emitting lasers, avalanche and PIN photodetectors. Special attention is given to the design and working characteristics of transmitters and pumping lasers for telecommunication networks. Prerequisite: BS in Physical sciences or Electrical or Computer Engineering. Spring, 3 credits, grading ABCF.

### **ESE 520 Applied Electromagnetics – CORE COURSE**

Wave phenomena and their importance in electromagnetic engineering. Harmonic waves. Phase and group velocities. Dispersive and nondispersive propagation. Transmission lines. Maxwell Equations. Uniform plane waves. Poynting's theorem, waveguides, resonators. Scattering matrix theory. Introduction to antenna theory. Electrostatics and magnetostatics as special cases of Maxwell equations. Prerequisite: Bachelor's degree in Physical Sciences. Spring, 3 credits, grading ABCF.

### **ESE 521 Applied Optics**

This course teaches students the fundamental techniques necessary for analyzing and designing optical systems. Topics include matrix methods for ray optics, fundamentals of wave optics, beam optics, Fourier optics and electromagnetic optics. The latter part of the course will deal with optical activity in anisotropic media and include polarization and crystal optics, electro-optics and acousto-optics. Fall, 3 credits, grading ABCF.

### **ESE 522 Fiber Optic Systems**

This course covers the essential components of a modern optical fiber communication system. Following a brief review of optical sources and characterization of optical fiber waveguides the remainder of the course examines the design of digital fiber optic links, single wavelength fiber-optic networks and wavelength division multiplexing. Fall, 3 credits, grading ABCF.

### **ESE 524 Microwave Acoustics**

Continuum acoustic field equations. Wave equation, boundary conditions and Poynting vector. Waves in isotropic elastic media: Plane-wave modes, reflection and refraction phenomena, bulk-acoustic-wave (BAW) waveguides, surface acoustic waves (SAW's). Plane and guided waves in piezoelectric media. BAW transduction and applications: delay-line and resonator structures, the Mason equivalent circuit, monolithic crystal filters, IM CON dispersive delay lines, acoustic microscopes, SAW transduction and applications: the interdigital transducer, band-pass filters, dispersive filters, convolvers, tapped delay lines, resonators. Prerequisite: ESE 319. Fall, 3 credits, grading ABCF.

### **ESE 525 Modern Sensors**

Sensors are devices that convert physical values into electrical signals. This course will provide practical information on diversified subjects related to the operation principles, design and use of various sensors. Established and novel sensor technologies as well as problems of interfacing various sensors with electronics are discussed. Objectives of the course are; Operation principles of sensors. Practical implementation of the sensors for scientific, industrial and consumer application. Useful and noise signals generated by sensors. Design of the sensor system. Spring, 3 credits, grading ABCF.

### **ESE 526 Silicon Technology for VLSI**

This course introduces the basic technologies employed to fabricate advanced integrated circuits. These include epitaxy, diffusion, oxidation, chemical vapor deposition, ion implantation lithography and etching. The significance of the variation of these steps is discussed with respect to its effect on device performance. The electrical and the geometric design rules are examined together with the integration of these fabrication techniques to reveal the relationship between circuit design and the fabrication process. Fall, 3 credits, grading ABCF.

### **ESE 527 Circuit Theory and Applications**

Foundations of design procedures for electric circuits. Fundamental concepts, graph theory, network equations, network functions, state equations, network synthesis, scattering parameters, nonlinear circuits. Fall, 3 credits, grading ABCF.

### **ESE 528 Communication Systems – CORE COURSE**

This course provides a general overview of communication theory and addresses fundamental concepts in this field. After a review of signals and systems representations, various continuous and digital modulation schemes are analyzed. Spread spectrum systems and their application to multiuser communications are also addressed. Advanced communication systems are described and general concepts of wide and local area networks are introduced. Fall, 3 credits, grading ABCF.

### **ESE 529 Electrical Network Theory**

Linear and nonlinear electrical networks; graph theory; determination of operating points; transient estimation; interconnection networks; numerical methods; parameter extraction; infinite and transfinite networks; discrete potential theory; random walks on networks. Spring, 3 credits, grading ABCF.

### **ESE 530 Computer-Aided Design**

The course presents techniques for analyzing linear and nonlinear dynamic electronic circuits using the computer. Some of the topics covered include network graph theory, generalized tableau and hybrid analysis, companion modeling, Newton's method in n-dimensions, numerical integration, sensitivity analysis, and optimization. Prerequisite: B.S. in electrical engineering. Spring, 3 credits, grading ABCF.

### **ESE 531 Detection & Estimation Theory**

Hypothesis testing and parameter estimation. Series representation of random processes. Detection and estimation of known signals in white and non-white Gaussian noise. Detection of signals with unknown parameters. Prerequisite: ESE 503 or permission of instructor. Spring, 3 credits, grading ABCF.

### **ESE 532 Theory of Digital Communication - CORE COURSE**

Optimum receivers, efficient signaling, comparison classes of signal schemes. Channel capacity theorem, bounds on optimum system performance, encoding for error reduction, and the fading channel. Source coding and some coding algorithms. Prerequisite: ESE 503 or permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 533 Convex Optimization and Engineering Applications**

Introduction to convex optimization and its applications; Convex sets, function, and basics of convex analysis; Linear and quadratic programs, second-order cone and semidefinite programming, geometric programming. Duality theory and optimality conditions; unconstrained minimization methods; Interior-point methods; Non-differentiable problems; Decomposition methods. Applications in engineering fields including statistical signal processing, communications, networking, energy systems, circuit design, and machine learning. Spring, 3 credits, grading ABCF.

### **ESE 534 Cyber Physical Systems**

This course covers important areas from the research literature on cyber-physical systems. Three application domains are emphasized: medical devices for health care, smart transportation systems, and smart buildings. Several key cross-cutting principles, independent of the application domain are also covered, including formal modeling, embedded systems, real time systems, feedback control, and sensor networks. Prerequisite: Background in embedded systems and computer networking is necessary. Fall, 3 credits, grading ABCF.

### **ESE 535 Information Theory and Reliable Communications**

Measures of information: entropy, relative entropy and mutual information. The asymptotic equipartition property. Lossless source coding: Kraft inequality and the source coding theorem. Introduction to error correcting codes. Continuous and waveform channels. Rate-distortion theory. Prerequisite: ESE 503 or equivalent or permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 536/CSE 626 Switching and Routing in Parallel and Distributed Systems (cross listed)**

This course covers various switching and routing issues in parallel and distributed systems.

Topics include message switching techniques, design of interconnection networks, permutation, multicast and all-to-all routing in various networks, non-blocking and re-arrangeable capability analysis and performance modeling. Prerequisites: ESE 503 and 545 or CSE 502 and 547, or permission of the instructor. Fall, 3 credits, grading ABCF.

### **ESE 537 Mobile Sensing Systems & Applications**

This is a graduate course focusing on recent advances and developments in mobile sensing systems and their applications, especially those leveraging modern mobile devices and embedded sensors. Topics include: conventional mote-class sensor networks, participatory sensing leveraging mobile devices, intelligent hardware and Internet-of-Things, location sensing, future information centric networking, and applications in smart homes, buildings, transportation, environment and health/fitness. Students need to read latest literature and write reviews, work on research problems and develop solutions, present their work and write formal reports. The practice of the basic research skills are major components. The course intends to be self-sufficient and prior experiences in programming, mobile devices and embedded systems is a plus. Fall, 3 credits, grading ABCF.

### **ESE 538 Nanoelectronics**

The major goals and objectives are to provide graduate students with knowledge and understanding of physical background and applications of nanoelectronics. The course will cover electrical and optical properties of materials and nanostructures, fabrication of nanostructures, nanoelectronic devices including resonant-tunneling devices, transistors, and single-electron transfer devices, as well as applications of nanotechnologies in molecular biology and medicine. Spring, 3 credits, grading ABCF.

### **ESE 540 Reliability Theory**

Theory of reliability engineering. Mathematical and statistical means of evaluating the reliability of systems of components. Analytical models for systems analysis, lifetime distributions, repairable systems, warranties, preventive maintenance and inspection. Software reliability and fault tolerant computer systems. Prerequisite: ESE 503 or permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 541 Digital System Design - This course is only for students in the Optoelectromechanical Systems Eng. Program and cannot be used to fulfill any ESE degree requirement.**

This course provides an introduction to digital and computer systems. The course follows a top-down approach to presenting design of computer systems, from the architectural-level to the gate-level. VHDL language is used to illustrate the discussed issues. Topics include design hierarchy and top-down design, introduction to hardware description languages, computer-aided design and digital synthesis, basic building blocks like adders, comparators, multipliers, latches, flip-flops, registers etc., static and dynamic random access memory, data and control buses, fundamental techniques for combinational circuit analysis and design, sequential circuit design procedures, and programmable logic devices. Testing of digital designs is addressed throughout the course. A mini project will complement the course. Spring, 3 credits, grading ABCF.

### **ESE 542/MEC 525 Product Design Concept Development and Optimization (cross listed)**

This course will concentrate on the design concept development of the product development cycle, from the creative phase of solution development to preliminary concept evaluation and selection. The course will then cover methods for mathematical modeling, computer simulation and optimization. The concept development component of the course will also cover intellectual property and patent issues. The course will not concentrate on the development of any particular class of products, but the focus will be mainly on mechanical and electromechanical devices and systems. As part of the course, each participant will select an appropriate project to practice the application of the material covered in the course and prepare a final report. Prerequisite: Undergraduate electrical or mechanical engineering and/or science training. Fall, 3 credits, grading ABCF.

### **ESE 543 Mobile Cloud Computing**

Introduction to the basic concepts of mobile cloud computing, including: 1. the mobile computing technology used in modern smart phones; 2. the cloud computing technologies used in existing data centers; 3. the synergy of mobile and cloud computing and its applications; 4. Programming on smart phone utilizing data center services. Students will gain knowledge of: the fundamental principles, the major technologies that support mobile cloud computing, the current challenges and primary areas of research within the field, and a basic understanding of the role of mobile cloud computing in the context of the everyday living. Spring, 3 credits, grading ABCF.

### **ESE 544 Network Security Engineering**

An introduction to computer network and telecommunication network security engineering. Special emphasis on building security into hardware working with software. Topics include encryption, public key cryptography, authentication, intrusion detection, digital rights management, firewalls, trusted computing, encrypted computing, intruders and virus. Some projects. Prerequisite or co-requisite: ESE 546 OR ESE 548 Fall, alternate years, grading ABCF.

### **ESE 545 Computer Architecture – CORE COURSE**

The course covers uniprocessor and pipelined vector processors. Topics include: hierarchical organization of a computer system; processor design; control design; memory organization and virtual memory; I/O systems; balancing subsystem bandwidths; RISC processors; principles of designing pipelined processors; vector processing on pipelines; examples of pipelined processors. The course involves a system design project using VHDL. Prerequisite: ESE 218 or equivalent. Spring, 4 credits, grading ABCF.

### **ESE 546 Networking Algorithms and Analysis**

An introduction to algorithms and analysis for computer and telecommunication networks. Continuous time and discrete time single queue analysis. Algorithms for public key cryptography, routing, protocol verification, multiple access, error, codes, data compression, search. Prerequisite: ESE 503 or permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 547 Digital Signal Processing**

A basic graduate course in Digital Signal Processing. Sampling and reconstruction of Signals. Review of Z-Transform theory. Signal flow-graphs. Design of FIR and IIR filters. Discrete and fast Fourier transforms. Introduction to adaptive signal processing. Implementation considerations. Prerequisites: Senior level course in signals and systems. Fall, 3 credits, grading ABCF.

### **ESE 548 Computer Networks**

To present basic network principles and methods in a top-down approach. The course will introduce the material of high-level network applications, and motivate students to find out about networking aspects inside these applications. The course will provide the details of network services from the top layer (TCP/UDP) to lower layer (Ethernet). The areas to be covered are computer networks introduction, network applications, transport layer, network layer, link layer (LAN), wireless networks, and network security. Students are required to implement two projects for data transfer. Students will also use network tools to inspect communication networks in action. Spring 3 credits, grading ABCF.

### **ESE 549 Advanced VLSI System Testing**

This course is designed to acquaint students with fault diagnosis of logic circuits. Both combinatorial and sequential circuits are considered. Concepts of faults and fault models are presented. Emphasis is given to test generation, test selection, fault detection, fault location, fault location within a module and fault correction. Spring, 3 credits, grading ABCF.

### **ESE 550 Network Management and Planning**

This course provides an introduction to telecommunications and computer network management and planning. Network management is concerned with the operation of networks while network planning is concerned with the proper evolution of network installations over time. Network management topics include meeting service requirements, management operations, management interoperability and specific architectures such as Telecommunications Management Network (TMS), and Simple Network Management Protocol (SNMP). Network planning topics include planning problem modeling, topological planning design, heuristic and formal solution techniques. Fall, 3 credits, grading ABCF.

### **ESE 551 Switching Theory and Sequential Machines**

Survey of classical analysis and synthesis of combination and sequential switching circuits, followed by related topics of current interest such as error diagnosis and fail soft circuits, use of large-scale integration, logic arrays, automated local design. Prerequisite: ESE 218 or equivalent. Fall, 3 credits, grading ABCF.

### **ESE 552 Interconnection Networks**

Formation and analysis of interconnect processing elements in parallel computing organization. Topics include: SIMD/MIMD computers, multiprocessors, multicomputers, density, symmetry, representations, and routing algorithms. Topologies being discussed include: Benes, Omega, Banyan, mesh, hypercube, cube-connected cycles, generalized chordal rings, chordal rings, DeBruijn, Moebius graphs, Cayley graphs and Borel Cayley graphs. Prerequisite: ESE 545 or equivalent. Fall, 3 credits, grading ABCF.

### **ESE 553 A/D and D/A Integrated Data Converters**

This is an advanced course on analog integrated circuit design aspects for data converters. Topics include: continuous and discrete-time signals and systems; sampling theorem; ideal A/D and D/A converters; specifications and testing of data converters; basic building blocks in data converters: current sources and mirrors, differential gain stages, voltage references, S/H circuits, comparators: Nyquist D/A and A/D converters: principles of data conversion and circuit design techniques; over sampling data converters: low-pass and band-pass delta-sigma modulators, decimation and interpolation for delta-sigma data converters. The attending students must be acquainted with principles of transistor operation, function of simple analysis. Familiarity with SPICE is required. Fall, 3 credits, grading ABCF.

### **ESE 554 Computational Models for Computer Engineers – CORE COURSE**

This course covers mathematical techniques and models used in the solution of computer engineering problems. The course heavily emphasizes computer engineering application. Topics covered include set theory, relations, functions, graph theory and graph algorithms, computational complexity, ordering relations, lattices, Boolean algebras, combinations and algebraic structures. Fall, 3 credits, grading ABCF.

### **ESE 555 Advanced VLSI Circuit Design – CORE COURSE**

Techniques of VLSI circuit design in the MOS technology are presented. Topics include MOS transistor theory, CMOS processing technology, MOS digital circuit analysis and design and various CMOS circuit design techniques. Digital systems are designed and simulated throughout the course using an assortment of VLSI design tools. Prerequisite: BS in Electrical Engineering or Computer Science. Spring, 3 credits, grading ABCF.

### **ESE 556 VLSI Physical and Logic Design Automation**

Upon completion of this course, the students will be able to develop state-of-the-art CAD tools and algorithms for VLSI logic and physical design. Tools will address design tasks such as floor planning, module placement and signal routing. Also, automated optimization of combinational and sequential circuits will be contemplated. Prerequisite: BS in Computer Engineering/Science or Electrical Engineering. Spring, 3 credits, grading ABCF.

### **ESE 557 Digital Signal Processing II: Advanced Topics**

A number of different topics in digital signal processing will be covered, depending on class and current research interest. Areas to be covered will include the following: parametric signal modeling, spectral estimation, multirate processing, advanced FFT and convolution algorithms, adaptive signal processing, multidimensional signal processing for inverse problems. Students will be expected to read and present current research literature. Prerequisite: ESE 547 or permission of instructor. Spring, 3 credits, grading ABCF.

### **ESE 558 Digital Image Processing I**

Covers digital image fundamentals, mathematical preliminaries of two-dimensional systems, image transforms, human perception, color basics, sampling and quantization, compression techniques, image enhancement, image restoration, image reconstruction from projections, and binary image processing. Prerequisite: BS in engineering or physical or mathematical sciences. Spring. 3 credits, grading ABCF.

### **ESE 559 Digital Image Processing II**

The course material will proceed directly from DIP-I starting with image reconstruction from projections. After the basic projection theorems are developed, computerized axial tomography techniques will be examined in detail including forward and inverse random transformations, convolution, back-projection and Fourier reconstruction: nuclear magnetic resonance imaging and positron emission tomography will be similarly covered. Super resolution concepts will be developed and applied to a variety of remote sensing applications as well as digital image coding for efficient transmission of digital TV imagery. Prerequisite: ESE 558. Spring, 3 credits, grading ABCF.

### **ESE 560 Optical Information Processing**

The course is designed to give the student a firm background of the fundamentals of optical information processing techniques. It is assumed that the student is familiar with complex algebra and is conversant with the principles of linear system theory and Fourier transformation. The body of the course is concerned with the scalar treatment of diffraction and its application to the study of optical imaging techniques and coherent and incoherent optical processors. Prerequisite: Bachelor's degree in Physical Sciences. Spring, 3 credits, grading ABCF.

### **ESE 563 Fundamentals of Robotics I**

This course covers: homogenous transformations of coordinates; kinematic and dynamic equations of robots with their associated solutions; control and programming of robots. Prerequisite: Permission of instructor. Fall, 3 credits, grading ABCF.

### **ESE 565 Parallel Processing Architectures**

The course provides a comprehensive introduction to parallel processing. Topics include; types of parallelism, classification of parallel computers; functional organizations, interconnection networks, memory organizations, control methods, parallel programming, parallel algorithms, performance enhancement techniques and design examples for SIMD array processors, loosely coupled multiprocessors, tightly coupled multiprocessors will be discussed; a brief overview of dataflow and reduction machines will also be given. Prerequisite: ESE 545 or equivalent. Spring, 3 credits, grading ABCF.

### **ESE 566 Hardware-Software Co-Design of Embedded Systems**

This course will present state-of-the-art concepts and techniques for design of embedded systems consisting of hardware and software components. Discussed topics include system specification, architectures for embedded systems performance modeling and evaluation, system synthesis, and validation. The course is complemented by three mini-projects focused on designing and implementing various co-design methods. Prerequisite: ESE 545, ESE 554 and ESE 333.

Fall, 3 credits, grading ABCF.

### **ESE 568 Computer and Robot Vision**

Principles and applications of computer and robot vision are covered. Primary emphasis is on techniques and algorithms for three-dimensional machine vision. The topics include image sensing of three-dimensional scenes, a review of two-dimensional techniques, image segmentation, stereo vision, optical flow, time-varying image analysis, shape-from-shading, texture, depth-from-defocus matching, object recognition, shape representation, interpretation of line drawings, and representation and analysis of 3D range data. The course includes programming projects on industrial applications of robot vision. Prerequisite: BS in Engineering or Physical or Mathematical Sciences. Fall, 3 credits, grading ABCF.

### **ESE 570 Bioelectronics**

Origin of bioelectric events; ion transport in cells, membrane potentials; neural action potentials and muscular activity, cortical and cardiac potentials. Detection and measurement of bioelectric signals; impedance measurements used to detect endocrine activity, perspiration and blood flow; impedance cardiography, vector cardiography; characteristics of transducers and tissue interface; special requirements for the amplification of transducer signals. Fall, 3 credits, grading ABCF.

### **ESE 571 – Introduction to Auto ID Technologies**

Introductory course is first in a series of Auto ID systems, technologies and applications. The course covers theory and applications of important data-capture technologies, namely, barcodes, biometrics and RFID. Topics to be covered include: architecture of data-capture/Auto ID systems, barcodes: overview of 1-D and 2-D barcodes and other LOS technologies; biometrics: fingerprints, iris-scan, voice recognition and smart cards; radio frequency identification (RFID): fundamentals, near-field vs. far-field, UHF read range estimation, reader sensitivity limits, tag singulation and multiple access protocols, standards, privacy and security issues in RFID, real time location systems (RTLS), and wireless sensor networks, 3 credits, grading ABCF.

### **ESE 575 Advanced VLSI Signal Processing Architecture**

This course is concerned with advanced aspects of VLSI architecture in digital signal processing and wireless communications. The first phase of the course covers the derivation of both data transformation and control sequencing from a behavioral description of an algorithm. The next phase reviews the general purpose and dedicated processor for signal processing algorithms. This course focuses on low-complexity high-performance algorithm development and evaluation, system architecture modeling, power-performance tradeoff analysis. The emphasis is on the development of application-specific VLSI architectures for current and future generation of wireless digital communication systems. An experimental/research project is required. Prerequisite: ESE 355 or equivalent. ESE 305 or ESE 337 or equivalent. ESE 306 or ESE 340 or equivalent. ESE 380 or equivalent. Spring, 3 credits, grading ABCF.

### **ESE 580, 581 Microprocessor-Based Systems, Engineering I and II**

This course is a study of methodologies and techniques for the engineering design of microprocessor-based systems. Emphasis is placed on the design of reliable industrial quality systems. Diagnostic features are included in these designs. Steps in the design cycle are considered. Specifically, requirement definitions, systematic design implementation, testing, debugging, documentation and maintenance are covered. Laboratory demonstrations of design techniques are included in this course. The students also obtain laboratory experience in the use of microprocessors, the development of systems, circuit emulation and the use of signature and logic analyzers. Fall, Spring, 4 credits, each semester, grading ABCF.

### **ESE 585 Nanoscale Integrated Circuit Design**

This course describes high performance and low power integrated circuit (IC) design issues for advanced nanoscale technologies. After a brief review of VLSI design methodologies and current IC trends, fundamental challenges related to the conventional CMOS technologies are described. The shift from logic-centric to interconnect-centric design is emphasized. Primary aspects of an interconnect-centric design flow are described in four phases: (1) general characteristics of on-chip interconnects, (2) on-chip interconnects for data signals, (3) on-chip power generation and distribution, and (4) on-chip clock generation and distribution. Existing design challenges faced by IC industry are investigated for each phase. Tradeoffs among various design criteria such as speed-power-noise-area are highlighted. In the last phase of the courses, several post-CMOS devices, emerging circuit styles, and architectures are briefly discussed. At the end of the course, the students will have a thorough understanding of the primary circuit and physical level design challenges with application to industrial IC design. Prerequisite: ESE 555 or ESE 330 and ESE 355. Spring, 3 credits, grading ABCF.

### **ESE 588 Pattern Recognition**

Basic concepts of pattern recognition techniques are introduced, including statistical pattern recognition, syntactic pattern recognition and graph matching. Topics on Bayes decision theory, parametric and nonparametric techniques, clustering techniques, formal languages, parsing algorithms and graph matching algorithms are covered. Prerequisite: Stochastic Processes and Data Structures. Spring, 3 credits, grading ABCF.

### **ESE 591 Industrial Project in OEMS Engineering**

Students must carry out a detailed design of an industrial project in Optoelectromechanical Systems engineering. A comprehensive technical report of the project and an oral presentation are required. Fall, 3 credits, grading ABCF.

### **ESE 597 Practicum in Engineering (Internship) Non-Regular Course**

This course is for part-time and full-time students who will be on Curricular Practical Training (CPT). CPT is defined as training that is an integral part of an established curriculum. Participation is in private corporations, public agencies or non-profit institutions. Students will be required to have a faculty coordinator as well as a contact in the outside organization, to participate with them in regular consultations on their project and to submit a final report to both. Registration must have the prior approval of the Graduate Program Director. This course can only be taken once by master's students and the credits must count towards the degree completion. Summer only, variable credit (1-3), grading, S/U.

### **ESE 599 Research (for students in the Master's program) Non-Regular Course**

Fall and Spring, variable and repetitive credit, Grading S/U.

### **ESE 610 Seminar in Solid-State Electronics**

Current research in solid-state devices and circuits and computer-aided network design. Fall and Spring, 3 credits, grading ABCF.

### **ESE 670 Topics in Electrical Sciences**

Varying topics selected from current research topics. This course is designed to give the necessary flexibility to students and faculty to introduce new material into the curriculum before it has attracted sufficient interest to be made part of the regular course material. Topics include: a) Biomedical Engineering; b) Circuit Theory; c) Controls; d) Electronics Circuits; e) Digital Systems and Electronics; f) Switching Theory and Sequential Machines; g) Digital Signal Processing; h) Digital communications; i) Computer Architecture; j) Networks; k) Systems Theory; l) Solid State Electronics; m) Integrated Electronics; n) Quantum Electronics and Lasers; o) Communication Theory; p) Wave Propagation; q) Integrated Optics; r) Optical Communications and Information Processing; s) Instrumentation; t) VLSI Computer Design and Processing. Fall and Spring, variable and repetitive credit, grading ABCF.

### **ESE 691 Seminar in Electrical Engineering**

This course is designed to expose students to the broadest possible range of the current activities in electrical engineering. Speakers from both on and off campus discuss topics of current interest in electrical engineering. Fall and Spring, 1 credit, repetitive, grading S/U.

### **ESE 697 Ph.D. Practicum in Teaching – \* 3 credits required for Ph.D. degree - Non-Regular Course**

This course provides hands-on experience in classroom teaching. Other activities may include preparation and supervision of laboratory experiments, exams, homework assignments, and projects.

Final report that summarizes the activities and provides a description of the gained experience and a list of recommendations is required. 3 credits, grading ABCF.

**\*Prerequisite: G5 status and Permission of Graduate Program Director. Students must inform the department TWO weeks prior to the beginning of each semester, if they plan on taking ESE 697. The graduate program director will then assign you to a course.**

### **ESE 698 Practicum in Teaching Non-Regular Course**

This course enables graduate students to gain experience in teaching and interacting with students enrolled in an electrical and computer engineering courses. Students enrolled in ESE 698 are expected to perform various teaching duties required by the course instructor, such as attending lectures, providing office hours, holding review/recitation sessions, assisting in lab sections, grading, etc. Fall, Spring and Summer, variable and repetitive, grading ABCF.

**ESE 699 Dissertation (Research on Campus) Non-Regular Course**

Students should register for this if the major portion of their research will take place on Stony Brook University campus, Cold Spring Harbor or Brookhaven National Lab.  
Fall and Spring, variable and repetitive credit, grading S/U.

**ESE 700 Dissertation Research (Off Campus – Domestic) Non-Regular Course**

Students should register for this when a major portion of their research will take place off-campus but in the United States and/or U.S. provinces (please note that Brookhaven National Labs and Cold Spring Harbor are considered on-campus). All international students who register for ESE 700 must enroll in one of the graduate student employee insurance plans and should be advised by an International Advisor. Fall and Spring, variable and repetitive credit, grading S/U.

**ESE 701 Dissertation Research (Off Campus – INTERNATIONAL) Non-Regular Course**

Students should register for this when a major portion of their research will take place outside of the United States and/or U.S. provinces. In these cases, domestic students have the option of the health plan and may also enroll in MEDEX. Fall and Spring, variable and repetitive credit, grading S/U.

**International students should note the following:**

- International students who are in their home country are NOT covered by the mandatory health plan and must contact the Insurance Office for the insurance charge to be removed.
- International students who are not in their home country ARE charged for the mandatory health insurance. If they are to be covered by another insurance plan, they must file a waiver by the second week of classes. The charge will only be removed if the other plan is deemed comparable.
- All international students must receive clearance from an International Advisor.

**ESE 800 Full-Time Summer Research**

0 Credits, grading S/U.

\*\*\*\*\*

## **OUTSIDE OF DEPARTMENT: COURSE LISTING**

Please Note: The following are courses from outside of the ECE department that may be used by Computer Engineering majors (as stated by guidelines) toward achieving their degree.

### **CSE 504 Compiler Design**

Advanced topics in compilation, including memory management, dataflow analysis, code optimization, just-in-time compilation, and selected topics from compilation of object-oriented and declarative languages. Prerequisite: CSE 304 and CSE 307. Spring, 3 credits.

### **CSE 506 Operating Systems**

This course is an in-depth study of important concepts and techniques found in modern computer operating systems. An undergraduate course in operating systems is a prerequisite. The course focuses on in-depth study of such important issues as virtual memory, filesystems, networking, and multiprocessor support, with an eye to recent directions in these areas. Textbook readings are supplemented, where appropriate, by papers from the research literature. An important part of the course is the case study of an actual operating system. Students study the source code for this operating system, and do programming exercises and projects that involve modifying the operating system and measuring its performance. Prerequisite: CSE 306. Spring, 3 credits.

### **CSE 526 Principles of Programming Languages**

Programming language concepts and design, with emphasis on abstraction mechanisms. Topics include: language paradigms (procedural, object-oriented, functional and logic), language concepts (values, bindings, types, modules), and foundations (lambda calculus, denotational semantics). Examples will be drawn from several representative languages, such as C, Java, Standard ML and Prolog. Prerequisite: CSE 307. Spring, 3 credits.

### **CSE 533 Network Programming**

Socket and client-server programming, remote procedure calls, data compression standards and techniques, real-time protocols (e.g: audio chat, etc.) security and cryptography (specifically, application layer security issues (e.g., authentication), web-related programming (CGI, Java/Java Script, HTTP, etc.), network management (SNMP-based management, dynamic/CORBA-based management). Prerequisite: CSE 306 and CSE 310. Fall and Spring, 3 credits.

### **CSE 548/AMS542 Analysis of Algorithms**

Techniques for designing efficient algorithms, including choice of data structures, recursion, branch and bound, divide and conquer, and dynamic programming. Complexity analysis of searching, sorting, matrix multiplication, and graph algorithms. Standard NP-complete problems and polynomial transformation techniques. Prerequisite: CSE 373. Fall, 3 credits.